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Load and Harmonic Distortion Characterization of Modern Low-Energy Lighting under Applied Voltage Variation

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Abstract

This paper investigates the voltage level dependencies of different modern lighting types. Experiments are undertaken and the results used to formulate polynomial load models, presented in a ZIP format. These characterize active power, reactive power and harmonic current behaviors; are compatible with established power systems simulation practice; and are generally demonstrated to muster a good fit to the raw experimental data. Despite exceedingly high levels of current distortion being noted for several lamp instances, the attenuating effects of lowering voltage and harmonic diversity, as captured within a pair of diversity factor terms, are shown to help significantly alleviate the associated harmonic pollution impact on low voltage networks.

Keywords: load modeling, power quality, reactive power, total harmonic distortion, ZIP coefficients, harmonic diversity

1. Introduction

This paper investigates the non-linear characteristics of modern, commercially available Light Emitting Diode (LED) and Compact Fluorescent Lamp (CFL) luminaires, specifically how their behaviors vary with the applied voltage level of the electricity supply. Previous efforts to characterize these device types include the development of static load models in [1, 2]; the reporting of LED lamp attributes, including luminous efficacy and performance against ambient temperature, in [3]; and an assessment of the...
 harmonic distortion footprint of LED lamps in [4], CFLs in [5–9] and both technolo-
gies in [10, 11]. This paper seeks to further contribute to these themes by exploring
the relationship between applied voltage level and the harmonic current distortion and
power consumption characteristics of different luminaire types. Experimental results
demonstrate that a strong correlation exists between these performance metrics.

The efforts of this paper are important in the context of the ongoing evolution of
lighting technology, exemplified by developments such as the regulatory obliged demise
of General Incandescent Lamps (GILs) [12]. The direct displacement of a major linear
load type with non-linear alternatives [3, 9], such as CFLs [13] and latterly LED lamps
(projected to capture 90% of the U.S. lighting market by 2035 [14]), is perceived to
have altered typical end-user demand profiles [12].

The adaptation of a key constituent of feeder level demand has implications for
utilities, especially in terms of load modeling for low voltage (LV) networks. Various
research [5, 6] contends that the long-standing practice of aggregating various static
load models to simulate large scale system interactions may no longer prove sufficiently
accurate for networks in which substantive non-linear effects abound. Impacts upon
the continued effectiveness of established network management and optimization tech-
niques, such as Conservation Voltage Reduction (CVR), may also be considered [15].
This research contributes to the topic by arguing that static load models can be adapted
to this evolved purpose, and thus retain their validity. An updated method of charac-
terization for harmonic polluting devices is detailed, alongside a new approach for the
modeling of harmonic current phasors.

Moreover, the typically low power classification of many modern lamps poses future
power quality risks for utilities. The latest international standards [16] continue to
prescribe relaxed harmonic current distortion limits for lamps rated between 5 and
25 W. Despite their low power, the influence of many such devices may give rise to
pronounced distortive effects within LV networks \[9, 17\]. However, their aggregated impact may be overstated, if the harmonic attenuating interactions among collections of lamps are not additionally factored. Thus, in the interests of analytical rigor, the concept of harmonic diversity ought to be explored \[5, 6\].

A novel experimental approach is hereby introduced, from which a set of load models are borne that encapsulate device behaviors in relation to fundamental powers and non-fundamental harmonic currents. In light of the ambient temperature and supply distortion induced variations observed for the currents drawn by appliances in \[3\] and \[15\], the testing methodology presented in this paper ensures that raw measurements are obtained in a manner that is sufficiently isolated from the influence of either factor. The deliberations of IEEE standard 1459-2010 \[18\] provide an impetus for excluding the presence of voltage distortion within such tests. On the assumption that practical network conditions apply and total harmonic current distortion (THD\(_I\)) levels exceed 40 \%, an apparent power orientated simplification is espoused in \[18\], such that the power quality test results are used to accurately characterize non-linear device behaviors.

An established, polynomial based characterization method is outlined, through which raw measurements are translated into a set of voltage response driven load models, for a selection of modern lighting types. A singular model for harmonic current phasors is hypothesized, through which voltage led fluctuations for both the magnitude and phase angle attributes are encapsulated. This is demonstrated to provide a suitable means through which multiple, harmonic polluting, load instances may be aggregated, in such a manner that the attendant cancellation effects \[6, 11\] of harmonic diversity are factored. Consolidated, lump load modeling of harmonic polluting devices is therefore made possible. This can contribute to the accurate representation of harmonic producing loads within load flow simulations. Moreover, the characterization parameters obtained from this study are conveniently tabulated for future reference. To assist in
this endeavor, an angular metric for harmonic diversity is introduced and analyzed, alongside more established, current magnitude focused terminology. The remainder of the paper is organised as follows. §2 reviews load modelling approaches and relevant practices and standards for power quality. §3 describes the experimental procedure undertaken to capture the relationships between applied voltage level and the loading and harmonic current distortion attributes of selected luminaires. The influence of ambient temperature upon measured device characteristics is also considered. §4 presents the experimental results, followed by analysis of the corresponding effects of harmonic diversity in §5. A discussion of these results in relation to existing power quality standards and other published research [5, 6] is provided in §6. Finally, conclusions on the impact that high penetrations of modern luminaires may have on future networks are presented in §7 alongside some topics for further research.

2. Harmonic Distortion and Associated Load Modeling Challenges

Notable shifts have occurred in the load preferences of residential customers in recent years. These include a proclivity for consumer electronics and the displacement of conventional forms of lighting with low energy alternatives [13, 19]. This is reflected within the increasingly non-linear nature of the power flows observed within LV networks [6]. In time, undesirable associated phenomena may foster new operational complications for utilities, such as growing levels of $THD_I$ and voltage unbalance factor (VUF), and subsequent voltage quality effects therein [6, 9]. The ongoing validity of existing modeling practices, including the use of established static load models, ought to be scrutinized in this context.

2.1. Load Modeling

Several accepted static load modelling techniques exist, each of which characterizes the behavior of loads in response to steady-state variations in applied voltage and
system frequency \[20, 21\]. Among the most popular methods is the ZIP polynomial model \[21\]. This is a quadratic function of applied voltage that is typically used to model the active, \(P\), and reactive, \(Q\), powers drawn by a load \[1, 2, 15\]. Its composition is presented in equations (1) and (2), where \(P_0\) and \(Q_0\) denote the active and reactive powers observed at nominal voltage, \(V_0\). Further explanation of this model, and its three scalar coefficients, is provided in \[15\].

\[
P(V) = P_0 \cdot \left[ Z_p \cdot \left( \frac{V}{V_0} \right)^2 + I_p \cdot \left( \frac{V}{V_0} \right) + P_p \right]
\]

\[
Q(V) = Q_0 \cdot \left[ Z_q \cdot \left( \frac{V}{V_0} \right)^2 + I_q \cdot \left( \frac{V}{V_0} \right) + P_q \right]
\]

By definition, the three ZIP coefficients in each equation must sum to 1. Many modern load types are observed to exhibit non-linear behavior, whereby changes to the load current are not proportional to fluctuations in applied voltage level. Such behaviors tend to be encapsulated by varying degrees of constant current \((I_p, I_q)\) and constant power \((P_p, P_q)\) attributes, which \[21\] discusses in greater detail.

2.2. Power Quality Standards

In the absence of corrective compensation, non-linear loads will, by definition, modulate the incoming supply voltage to draw a distorted current waveform. For those appliances that draw less than 16 A, IEC standard 61000-3-2 \[16\] stipulates performance criteria, including harmonic current limits. These are delineated into one of four classifications, A to D, with Class C incorporating all forms of lighting. The limits specified for Class C devices in \[16\] are reproduced in Table 1, where \(\lambda\) denotes a device’s displacement power factor (DPF) \[1\].

\[1\] ratio of active to apparent power draw at fundamental frequency
The limits set out in Table 1 are eased for lamps with active power ratings in the 5 to 25 W band. These low power adjusted limits, which allot to three different permissible performance frameworks [16], are displayed as percentages of the prevailing fundamental current in Table 2. The first set of requirements, group (i), is revised from a set of mA/W specified limits, on the assumption that a nominal voltage of 230 V applies. Group (ii) combines comparatively loose magnitude limits with strict controls on current phase angle behaviors. Group (iii), is specified alongside an overall limit for THD$_I$ of 70 %. The delineation of harmonic current pollution standards at 25 W is discussed in other work [4, 6, 10, 11, 15], although each pertains to the previous revision of IEC 61000-3-2 (2014 [22]), in which all non-discharge lighting types rated below 25 W were entirely exempted from harmonic current restrictions.

2.3. Influence of Supply Voltage Distortion

IEEE standard 1459-2010 [18] describes how the coupling of the distinct concerns of total harmonic voltage distortion (THD$_V$), pertaining to the electricity supply, and THD$_I$, attributable to connected appliances, serve to magnify the deleterious effects of distortion across LV networks. As such, utilities in Europe are mandated, by the EN 50160 standard for voltage quality [23], to ensure that the ten-minute averaged levels of THD$_V$ observed at customer points of common coupling (PCCs) are within 8 %.

The interaction of THD$_V$ and THD$_I$ is generally observed to increase the distortive content of the apparent power drawn by harmonic current inducing loads [15]. This quantity is defined as Non-Fundamental Apparent Power, denoted $S_N$. For reference, further review of this quantity, and its derivation, is given in [15], from which it is shown that the ratio of $S_N$ to the fundamental component of apparent power, $S_1$, can be expressed, exclusively, as a function of THD$_V$ and THD$_I$. 

6
$$\frac{S_N}{S_1} = \sqrt{[THD_I^2 + THD_V^2 + (THD_I^2 \cdot THD_V^2)\right] \quad (3)}$$

In [15] and [24], different ZIP load modeling approaches are adopted to characterize the harmonic distortion ratios and non-fundamental apparent powers. For practical networks, THD$_I$ levels are often much higher than THD$_V$ and thus, as anticipated by (3), exert a more pervasive influence within the formation of harmonic power flows [15, 18]. Indeed, for conditions in which THD$_V$ < 5 % and THD$_I$ > 40 %, it is argued that $S_N \approx S_1 \cdot THD_I$ provides an acceptable approximation [18]. This tenet is validated in [15] where, given an expectation that THD$_V$ shall not exceed 8 % (in accordance with [23]), a set of ZIP models generated for THD$_I$ are observed to provide accurate estimates for rms apparent power ($S$), in the midst of applied voltage variations.

The observations of [15] demonstrate that desirable test conditions, for evaluating the harmonic ampere contributions of appliances and the complementary creation of voltage dependent, static load models, are best achieved by limiting THD$_V$ to very low levels. This ensures that cross projections of THD$_V$ and THD$_I$ upon device performance, and those associated distortion power quantities for which THD$_V$ is a factor [15, 18], are rendered negligible.

In effect, appliance behaviors, as observed under practicable conditions (THD$_V$ ≤ 8%), may reasonably be inferred (in terms of $S$) from $V_1$ dependent load models, alone.

This modelling proposition offers a reasonable approximation, but not an exhaustive characterization, of lamp behaviors under applied voltage variation (inclusive of a small degree of harmonic content). However, its limitations are acknowledged, especially the manner by which accuracy declines as THD$_V$ levels rise [15], and so while its performance remains acceptable for most practicable instances of supply distortion [15], it is best suited for application at low, or zero, levels of THD$_V$. To achieve a complete, voltage dependent characterization of non-linear device behaviors, especially
for harmonic current distortion, additional models for $I_h(V_n)$, $I_h(V_{n+1})$ ... etc, and their cross projections, must be developed. The prospective undertaking of any such additional efforts, may consider the trade off that exists; the small potential increases in modeling accuracy versus the extra computational burden and unlimited number of potential harmonic and interharmonic interactions that may need to be quantified.

The findings of [15] are expanded upon, to propose a method for characterizing harmonic current by generating ZIP models for individual harmonic orders rather than the overall $THD_i$ metric. The documented approach facilitates modeling of both the magnitude and phase angle properties of harmonic current, as functions of $V_1$.

2.4. Harmonic Diversity

The harmonic current signatures of different appliances diverge by magnitude and phase angle. Reasons for angular disparity include differences in system impedance, as observed at the PCC from the input terminals of a connected load [6]; the size of dc link capacitors [5] and loading conditions, taken with respect to device rated power [5].

For each harmonic current order, the spread of angles observed across multiple consumer devices gives rise to cancellation effects upon aggregation within adjoining LV networks. If a non-zero phase angle difference is observed between harmonic sources, the vector resultant of aggregate current shall be less than the equivalent arithmetic sum (of the respective harmonic magnitudes by device). This observation is summarized within a ratio term known as the magnitudinal harmonic diversity factor, $MDF_h$ [5, 6, 11], for which lower values signify a greater spread of harmonic phase angles and consequentially, a more pervasive cancellation effect. In [10], the term phase angle diversity factor is used to describe the same effect. For a harmonic current of order $h$, $I_h \angle \phi_h$, the magnitudinal diversity factor pertaining to the aggregate current drawn by $N$ different loads is:
Harmonic diversity is studied for some switch-mode power supply (SMPS) loads, including CFLs, in [5, 6, 10, 11], and LED lamps in [4, 10, 11]. In all cases, typical MDF levels are observed to gradually decline as more devices connect in parallel. As \( N \) increases further, an asymptotic level for \( \bar{MDF} \) is attained, beyond which cancellation benefits diminish [5]. In general, harmonic phase angle disparity with CFLs is narrower than with LED lamps, for which the cancellation effect is more pronounced [10 11].

An additional facet of this phenomenon is introduced and examined in this research, within a related metric termed the angular harmonic diversity factor, \( ADF_h \). This ratio term aims to quantify the influence of harmonic cancellation upon the resultant phase angle of the aggregated harmonic current signal. For harmonic currents of order \( h \), \( |I_{h,j}| \angle \phi_{h,j} \), the angular diversity factor pertaining to the amalgamated current drawn by \( N \) different loads is defined in (5). The numerator denotes the angle of the resultant current phasor (scaled by \( N \)), with the denominator indicating the maximum possible angular swing that may be exerted, for currents of order \( h \), as a consequence of aggregation. The introduction of this factor builds upon the studies in [25], where the harmonic current phase angles of various aggregate load mixes are shown to converge to a fixed value, for group sizes of thirty and over.

\[
ADF_h = \frac{N \cdot Arg\left(\sum_{j=0}^{N} I_{h,j}\right)}{\sum_{j=0}^{N} \left|Arg(I_{h,j})\right|} \tag{5}
\]

This theme is explored in the wider context of aggregate load modelling to encompass an extensive selection of electrical appliance types. In [25][27], a single, composite harmonic current footprint is resolved from the attributes of individual devices to form a consolidated lumped load model, also referred to as an aggregate harmonic load (AHL)
in [26]. In [25–27], ADLs are continuously updated, according to various load mixes and so are configured to reflect the time-varying nature of demand trends within practical networks. The creation of time series profiles for harmonic current is subsequently made possible, although in the surveyed literature this is only enacted for harmonic current magnitudes, rather than phase angle data. AHLs can be defined at various levels of network abstraction [25], such as from the perspective of a single household [27] or to characterise the entire secondary LV network served by a step-down transformer [26, 28]. This approach can help simplify the simulation of very large network models, in which detailed analysis of all LV sub-circuits may not be required [29].

In some literature, the non-linear constituent of AHLs are composed from the amalgamation of equivalent circuit models rather than polynomial based models [27, 28]. This method is based on prior research performed in [5], for which an inherent weakness is acknowledged in terms of its application to the aggregation of harmonic behaviors within the lumped SMPS models produced. This pertains to the “ability to correctly model harmonic cancellation between the individual SMPS loads” owing to the absence of consideration for harmonic phase angles within the constituent load circuit models. In [26], this limitation is countered via the probabilistic assignment of a synthetic magnitudinal diversity factor, which is used to scale the sum of harmonic current magnitudes to an appropriately attenuated aggregate level.

Conversely, the polynomial based load modelling technique, espoused for harmonic current characterization in this paper, does not require the additional synthesis of $MDF_h$ values. The natural harmonic cancellation effect is implicit within the method, such that the resolution of (4) with the ZIP model estimates is proven to be consistent with real world observations. Moreover, previous research has neglected the influence of supply voltage magnitude diversity across networks, in invoking a heightened sense of phase angle diversity between load instances, and thus a more pervasive cancellation
effect. This shortcoming is remedied within the models proposed by this paper.

3. Methodology

3.1. Apparatus

An overview of the test equipment is given in Fig. 1. It includes a relay test and calibration tool, which acts as a high-fidelity voltage source for the device-under-test (DUT), ensuring that the potentially degrading influence of supply voltage distortion on the results is effectively eliminated. This unit feeds a power meter, which serves as both a protective buffer for the signal source and a measurement verification aid for the experiment. This feeds power to the single-phase connected DUT and provides voltage monitoring access to power quality data logging apparatus. Current is sensed as it flows along the neutral return wire from the DUT to the power meter. Fundamental and rms current; active, reactive and non-fundamental power; power factor; and the magnitudes and phases angles of non-fundamental, harmonic voltages and currents; are logged at one second intervals. The logging apparatus also quantifies $THD_I$ results through the accumulation of measurements for each harmonic current order, up to $h = 40$.

3.2. Experimental Procedure

To obtain a valid ZIP load representation of a luminaire, the impact of ambient temperature on its performance must be isolated from that of voltage, as discussed in [3]. One means of mitigation is to minimise the temporal variation in temperature observed across each experiment, as the applied voltage is adjusted to different levels. This is achieved by monitoring temperature within the DUT enclosure using a thermocouple device. A testing method approved by the Illuminating Engineering Society, IES LM-79-08 [30], was chosen from which an appropriate temperature range of $25 \pm 1^\circ C$ was defined for application within each experiment.
Experiments begin by powering on the selected DUT at nominal voltage until the enclosure warms beyond 24°C [30]. Each voltage level applied to the DUT is maintained for a period of two minutes; during which power quality data is logged on a per second basis, while temperature readings are recorded at one-minute intervals.

For LED modules the applied voltage is increased in steps of 0.02 pu across the statutory range (0.90 - 1.10 pu [23]). Additional care is taken while testing CFLs. For each voltage increment, the ensuing power transitions are observed to take roughly twenty minutes to subside. Consequently, a delay of thirty minutes is applied after each new voltage level is asserted, prior to the logging of performance data. This approach is similar to [4], in which ten-minute stabilization delays are applied during testing. A larger voltage interval of 0.05 pu is applied between readings for CFL testing, so that both the variation in temperature observed across the full body of readings and the subsequent effects upon ZIP load characterization are minimized.

In [4], adjustments to voltage are applied according to the SEMI F47 standard curve for voltage sag immunity testing [31]. This aspect contrasts to the linear ramping of voltage undertaken for this paper, across the statutory range.

3.3. Formulation of ZIP coefficients from Experimental Data

The ZIP coefficients are determined via least squares regression. Considering the ZIP model for active power (1), the process begins by computing the least squares fit of the quadratic function

$$\hat{P} = \theta_1 V^2 + \theta_2 V + \theta_3$$

(6)

to the measured P-V data. By denoting the n-length vector of power measurements as $y = [P_1 \ldots P_n]^T$, constructing matrix $X$ from the corresponding voltages as
and defining the vector of ZIP load coefficients as \( \theta = [\theta_1 \theta_2 \theta_3]^T \), the quadratic function can be expressed in matrix vector form as \( \hat{y} = X\theta \). The least squares fit to the true data \( y \) is then obtained using the Moore-Penrose pseudo inverse approach:

\[
\theta = (X^TX)^{-1}X^Ty
\]

(8)

Having computed \( \theta \) in (8), the fitted value for \( P_0 \) is obtained from the solution for (6) in which \( V \) is set equal to \( V_0 \). ZIP parameters \( Z_p, I_p \) and \( P_p \), are then given by:

\[
\begin{bmatrix}
Z_p \\
I_p \\
P_p
\end{bmatrix} = \frac{1}{P_0} \begin{bmatrix}
V_0^2 & 0 & 0 \\
0 & V_0 & 0 \\
0 & 0 & 1
\end{bmatrix} \begin{bmatrix}
\theta_1 \\
\theta_2 \\
\theta_3
\end{bmatrix}
\]

(9)

An advantage of this approach is that the \( Z_p + I_p + P_p = 1 \) condition is automatically satisfied without further augmentation of the least squares problem. To engender confidence in the reported values, the final ZIP coefficients are specified alongside the coefficient of determination \( (R^2) \) that arises from the direct comparison made of the relevant \( y \) and \( \hat{y} \) data sets, as observed across the same set of applied voltage samples.

The methodology for obtaining \( R^2 \) from the experimental datasets that pertain to each tested lamp is described in the following. First, the regression sum of squares, \( SS_{reg} \), metric is formed, via (10). This compares the predicted values with the mean of the data set.
\[ SS_{reg} = \sum_{j=0}^{N} (\hat{y}_j - \bar{y})^2 \] (10)

Second, the residual sum of squares, \( SS_{res} \), is calculated from (11), to quantify the prediction error between the raw and modeled data, whereby an error array is created via: \( e_j = y_j - \hat{y}_j \).

\[ SS_{res} = \sum_{j=0}^{N} e_j^2 \] (11)

Finally, \( R^2 \) is given by the quotient:

\[ R^2 = \frac{SS_{reg}}{SS_{reg} + SS_{res}} \] (12)

3.4. Characterization of Harmonic Current Draw as a function of Applied Voltage

The use of ZIP models to characterize voltage derived fluctuations in \( P \) and \( Q \) is common electrical engineering practice. This approach can be expanded to define similar model parameters for \( THD_I \) [15] or the individual harmonic current components drawn by the tested lamps. Given that ZIP polynomials are identified as being “without physical correspondence” [20], it is acceptable to frame the relationship between harmonic currents and applied voltage in this manner.

The magnitude and angle properties of each harmonic current order, \( h \), are often presented as a singular polar variable. However, the associated ZIP characterization is expressed more conveniently in cartesian form; consisting of two orthogonal variables, \( a_h \) and \( b_h \). These pertain to two distinct load models; each expressed as a quadratic function of \( V_1 \) and \( h^{th} \) order specific sets of ZIP coefficients, such as \( \theta_{ha} \) for \( a_h \)

\[ \hat{a}_h(V_1) = a_{0,h} \cdot \left[ \theta_{ha,1}V_1^2 + \theta_{ha,2}V_1 + \theta_{ha,3} \right] \] (13)

and \( \theta_{hb} \) for \( b_h \)
\[
\hat{b}_h(V_1) = b_{0,h} \cdot \left[ \theta_{h,1} V_1^2 + \theta_{h,2} V_1 + \theta_{h,3} \right]
\]

(14)

where \( a_h = |I_h| \cdot \cos(\phi_h) \) and \( b_h = |I_h| \cdot \sin(\phi_h) \), with the terms \( a_{0,h} \) and \( b_{0,h} \) being used to signify the respective values observed at nominal voltage, \( V_0 \).

The corresponding singular load model for each order is presented in (15). Similarly, the ZIP models expressed in (1)-(2) for \( P \) and \( Q \) are an extension of (15), where \( h = 1 \) and the current phasor, \( |I_1| \angle \phi_1 \), is scaled by reference voltage phasor, \( V_1 \).

\[
|\hat{I}_h(V_1)| \angle \hat{\phi}_h(V_1) = \hat{a}_h(V_1) + j \hat{b}_h(V_1)
\]

(15)

To aid comprehension of the stated method, voltage led variations in the harmonic currents drawn by one tested lamp (Bulb 05, introduced in §4) are interpreted via polar plots. Similar portrayals of harmonic currents are found in [4, 5, 10, 25, 29]. In Fig. 2, current phasors of the six lowest, odd, non-fundamental orders, plus the fundamental, are illustrated in absolute mA terms. Even harmonic orders are discounted from the analysis. This adheres with a convention for power systems modelling in the steady state [32], in which symmetry between the positive and negative half-wave cycles of voltage and current signals is always assumed. The stated selection of non-fundamental, harmonic orders is also reasoned to contribute an equitable spread of negative (orders 5 and 11), positive (orders 7 and 13) and zero (orders 3 and 9) sequence effects [32], within any prospective, harmonic load flow study that may seek to incorporate the load models expounded by this research. Moreover, for each of the eleven tested lamps, the bulk of the recorded \( THD_I \) measurements is observed to accumulate within the six chosen harmonic current orders.

The values displayed in Fig. 2 are determined from the ZIP model characterization of experimental measurements for Bulb 05. The logged levels of \( THD_I \), pertaining to these polar plots, are observed to decline with applied voltage, from 145 % at 1.10 pu
to 123 % at 0.90 pu. The circular perimeter of each plot denotes a reference current of 50 mA. Despite the reduction in $THD_I$, in percentage terms, the attendant ampere magnitude of fundamental current is shown to grow with decreasing voltage in Fig. 2(g), such that an overall increase in rms current\(^2\) is observed, a reflection of the constant power nature of the device. The harmonic phase angle associated with the fundamental corresponds to the displacement power factor angle, $\phi$. Fig. 2(g) shows how, for Bulb 05, $\phi$ increases and thus $DPF$ (the cosine of $\phi$) reduces as the voltage level declines. All other non-fundamental phasors are observed to rotate by varying angular amounts as the $V_1$ level is adjusted.

4. Experimental Results

Details of the evaluated lamps are given in Table 3. These include a 60 W rated GIL (Bulb 11) that chiefly serves as a performance reference for the CFL and LED lamps. The active and reactive power responses of each lamp are plotted against voltage level in Fig. 3 and Fig. 4. The reactive power contribution of the GIL is deemed negligible when compared to other bulbs, and so is omitted from Fig. 4.

Nominal levels of active and reactive power, $P_0$ and $Q_0$, are specified as those values observed at 1.0 pu voltage. As such, each of the per-unit characteristics depicted in Fig. 3 and Fig. 4 are shown to pass through the (1, 1) co-ordinate. Actual measured data is depicted by the scattered points, while full line trends chart the corresponding ZIP load estimates. Ambient temperature was successfully kept within the bounds recommended by [30] throughout. Table 3 also includes the ZIP coefficients determined for all evaluated lamps. The values reported for $Q_0$ show that the reactive power contributions of all lamps are negative, and thus capacitive in nature.

For all CFLs, $|I_1|$ is recorded as decreasing with lowering voltage, while for all LED

\[^2\] $I = I_1 \cdot \sqrt{THD_I^2 + 1}$
lamps it is noted to rise. The DPF of all CFLs and LED lamps 01, 02 and 05 degrades with reducing voltage, but is shown to improve for LED lamps 03, 04, 06 and 07.

Tables 4 to 9 each present two sets of ZIP coefficients, determined for the cartesian form of non-fundamental harmonic currents $I_3$ to $I_{13}$ (odd orders only), across all evaluated lamps. Variables $a_{0,h}$ and $b_{0,h}$ are analogous to that of $P_0$ and $Q_0$ in Table 3, and pertain to the nominal magnitudes of $a_h$ and $b_h$, for a given harmonic order $h$. The polar form of $I_h$, expressed in percentage terms of $I_1$, is resolved within the two furthest right-hand columns of each table.

Regarding $I_3$ (Table 4), across all tested CFLs, GILs and LED lamp 07, lowering voltage proves effective in reducing $|I_3|$. For LED lamps 02, 03, 04 and 05, $|I_3|$ is observed to increase with declining voltage. For the other LED lamps (01 and 06), reductions in voltage have a negligible effect. The contrasting influence of voltage reduction on $|I_3|$ is charted in mA terms for (a) LED lamp 02 and (b) CFL lamp 09 in Fig. 5. As applied voltage reduces from 1.10 pu to 0.90 pu, a 19.7% increase in $|I_3|$ is noted for the former, while a 4.7% reduction is observed for the CFL.

Table 5 reveals that for all tested CFL, GILs and LED lamps 01, 03, 04, 06 and 07, lowering voltage is shown to be effective in reducing levels of $|I_5|$. However, for LED lamps 02 and 05, $|I_5|$ is observed to increase (in ampere terms) as voltage decreases. Conversely, lower voltages are shown to be effective in reducing $|I_7|$ for all lamps. Apart from LED lamp 06 and CFL 08, $|I_9|$ is observed to decline as voltage is reduced (Table 7). Likewise, general decreases in $|I_{11}|$ are observed upon the application of voltage reduction, with exceptions noted for LED lamps 03, 05 and 07 (Table 8). Increases in $|I_{13}|$, with decreasing voltage, are observed for LED lamps 02, 05 and 07 (Table 9).

In the interests of validation, $R^2$ values are also reported within each set of results in Tables 3 to 9. The vast number of these tend towards the ideal value of 1.00, and so indicate the success of the ZIP models in engendering high quality fits against the
underlying raw data. However, a small number of low $R^2$ instances are also present, which exposes the limitations of both the precision of the measurement equipment and the analytical approach, encapsulated by equations (1), (2), (13) and (14).

Disparities between the $R^2$ values obtained among cartesian paired terms, $\hat{a}_h$ and $\hat{b}_h$, are noted for several harmonic current phasors in Tables 4 to 9. This phenomenon is scrutinized in greater depth for LED lamp 04 (and phasor $I_5$), LED lamp 07 (and phasors $I_3$ and $I_9$) in Figs. 6, 7 and 8, respectively. These charts highlight how the presence of a poor/good fit combination within a cartesian pair translates into a similar discrepancy among the corresponding polar terms, $|I_h|$ and $\phi_h$.

5. Harmonic Diversity

In [6], the aggregated harmonic distortion impact of CFLs within LV networks is explored using a Monte Carlo approach. Network impedance, integrated dc link capacitance and fluorescent tube power are varied in simulation and the ensuing harmonic cancellation effect is analyzed. A similar method is adopted in this paper to investigate how fluctuations in applied voltage influence the extent of harmonic cancellation observed for groups of CFLs and LED lamps. In a practical sense, such analysis may prove useful in the context of an LV distribution circuit, along which an assortment of customers are connected. Despite the influence of system impedance on the harmonic diversity effect not being explicitly analyzed within this research, it is known from other literature [5, 6, 25] that the attenuation of harmonic current distortion among aggregated loads is expected to be more pervasive as system impedance increases, especially in the case of higher order harmonics. This finding is particularly pertinent within less stiff regions of the grid [5], such as radial sections of the distribution network.

In the first study, $N$ instances of a ZIP model pertaining to a specific lamp type are each subjected to a randomized applied voltage between 0.90 to 1.10 pu. The
harmonic currents from the homogeneous, \( N \) lamp grouping are summed and the vector and arithmetic resultants used to generate diversity factors for each harmonic order, \( MDF_h \) and \( ADF_h \), as per (4) and (5). For each order, \( h \), this step is replicated for one thousand different randomized voltage permutations, from which mean diversity factors, \( \overline{MDF}_h \) and \( \overline{ADF}_h \), are computed. This process is repeated across different group sizes, whereby \( N \) is increased progressively from 2 to 100, and for each lamp type analyzed in this paper. Due to lamp type being common across each \( N \)-sized grouping, the \( MDF \) values reflect the magnitudinal harmonic cancellation potential due to a single variable, voltage variation. Annotated plots of the cancellation impact attributable to voltage variation are presented in Fig. 9(a) for all seven LED lamps and Fig. 9(b) for the three CFLs.

In Fig. 9, diversity led attenuation appears to be more prevalent within homogeneous groups of LED lamps than for CFLs. More generally, the cancellation impact is observed to be more pronounced for higher harmonic orders, with a negligible influence observed for the third and fifth orders. Moreover, as is the case in [6], the diversity effect grows for further increments of \( N \), reaching asymptotic levels for \( \overline{MDF}_h \) in Fig. 9 once group size reaches 50 lamps or more.

The second study is conducted on a similar basis. However, each \( N \)-sized grouping is composed from a mixed combination of either LED or CFL lamp type instances, rather than a homogeneous collection. Each combination is chosen at random, within which multiple instances of one lamp type may appear. Given the increased degrees of freedom, the sample size for each Monte Carlo experiment is raised to ten thousand. The resultant \( \overline{MDF}_h \) and \( \overline{ADF}_h \) values reflect the combined harmonic cancellation potential of voltage variability and technological diversity. Plots of the magnitudinal cancellation impact are presented in Fig. 10(a) for LED lamps and Fig. 10(b) for CFLs, while the similarly asymptotic nature of the angular harmonic diversity factor
is observed in Fig. 11(a) for the same mix of LED lamps and Fig. 11(b) for CFLs.

In general, the magnitudinal diversity factors recorded across a mix of bulbs are much lower than those observed for multiple instances of the same bulb and so collections of this nature exhibit higher degrees of harmonic attenuation. The cancellation potential for multiple LED lamps appears to be more pervasive than with CFLs, as anticipated by [10][11]. Once again, the attenuation of higher order harmonics is more substantial, although significant reductions in the third and fifth harmonics are noted among the collection of LED lamps (a converged $MDF_h$ level of around 0.75).

These results show how the theory of harmonic diversity is inherently factored within the voltage driven, harmonic current phasor model introduced in (15) to create an holistic expression for fluctuations in aggregate harmonic currents on LV networks, as a function of the individual PCC voltages. This contrasts with the equivalent circuit based approach for non-linear load modelling in [5], in which harmonic cancellation is not apparent within the corresponding aggregate models. In this paper, for a sufficiently large group size, the expected magnitudinal diversity factor (4), $MDF_h$, pertaining to harmonic current order, $h$, is demonstrated by Figs. 9 and 10 to tend towards an asymptotic level. This outcome echoes the findings of [25], in which convergence is observed for the third and fifth harmonic current orders for $N > 30$. Moreover, the $MDF_h$ values reported for the practical investigation of CFLs and LED lamps in [11] are similar to those presented in this paper.

For circumstances in which harmonic phase angle data is not available but the expected value of $MDF_h$ is known, an expectation of the attenuated magnitude of the aggregate harmonic current, attributable to a collection of multiple lamp instances, may be determined from the sum of the expected individual phasor magnitudes.

$$E \left[ \left| \sum_{j=0}^{N} \hat{I}_{h,j}(V_{1,j}) \right| \right] = MDF_h \cdot \sum_{j=0}^{N} E \left[ \left| \hat{I}_{h,j}(V_{1,j}) \right| \right]$$

(16)
Similarly, for large group sizes the asymptotic value of the mean angular harmonic diversity factor $ADF_h$, may be used to recover the expected phase angle of the aggregated harmonic current phasor, from the sum of the absolute values taken for each of the expected individual harmonic phase angles.

$$E\left[ \text{Arg} \left( \sum_{j=0}^{N} \hat{I}_{h,j}(V_{1,j}) \right) \right] = \frac{1}{N} \cdot ADF_h \cdot \sum_{j=0}^{N} E \left[ \left| \text{Arg} \left( \hat{I}_{h,j}(V_{1,j}) \right) \right| \right]$$ (17)

6. Discussion

Fig. 3 shows how the active power consumption of modern lamps is consistently less responsive to changes in voltage than legacy types, i.e. GILs. For LED lamps, this mostly manifests in close to constant power behavior. By contrast, the active power drawn by CFLs is shown to be more susceptible to changes in voltage. The values reported for $Q_0$ in Table 3 highlight that the reactive power contributions of all tested lamps is nominally capacitive in nature. In Fig. 4, the reactive power of five of the tested LED lamps is observed to correlate positively with voltage, while for two bulbs (02 and 05) it is negatively correlated, rising in magnitude as voltage is reduced. For each CFL tested, reactive power is shown to gradually decline with decreases in voltage.

Results for nominal harmonic current magnitudes, denoted $|I_{0,h}|$ in Tables 4 to 9, allude to significant levels of distortion for most LED lamps. For LED bulbs 01, 02 and 05, the nominal magnitudes of the third (Table 4), fifth (Table 5) and seventh (Table 6) harmonics exceed all the corresponding choices of requirements defined for lighting in Table 2, but would not have violated the previous, 2014 revision of IEC 61000-3-2 [22], given its sub-25 W exemption for non-discharge lighting types [15]. While $|I_{0,3}|$ and $|I_{0,5}|$ emissions from CFLs (08, 09 and 10) are substantial, they adhere with the group (ii) set of limits in Table 2. Marginal excursions of the seventh harmonic limit are noted for CFLs 09 and 10 in Table 6. However, given the prospective cancellation
effect of lamp aggregation observed for these two devices (Fig. 10), the cumulative
impact of connecting numerous CFLs to LV networks may prove to satisfy each of the
requirements options in Table 2 if one considers average bulb performance.

Nominal operation of LED lamps 01, 02 and 05 and all three CFLs (08, 09 and 10),
at 230 V, transgresses beyond the group (i) magnitude limits defined for both the ninth
(Table 7) and eleventh (Table 8) harmonic currents in Table 2. Likewise, the limit for
the thirteenth harmonic (6.82%; Table 2, group (i)) is exceeded for each of these six
lamps, plus LED lamp 06 (Table 9). LED lamp 03 is consistently shown to exhibit low
levels of harmonic current distortion across all orders. This device is thought likely to
incorporate active filtering circuitry, similar to that discussed in [5].

The influence of voltage reduction on $THD_I$ is more difficult to quantify than for $P$
and $Q$, given its impact on both the magnitude and angle of harmonic current phasors.
Per-unit variations in total non-fundamental harmonic current, $I_H$, are plotted against
applied voltage, within Fig. 12 for each tested lamp. This chart shows that every tested
CFL emits lower harmonic currents at lower voltage levels, as do all LED lamps with the
exception of bulbs 02 and 05. In general, one may conclude that voltage management
strategies, such as CVR, may be expected to help suppress harmonic current emissions
attributable to lighting, through the practice of lowering of PCC voltages.

In §4, the importance of the $R^2$ results was stressed and some instances of poor fit
were highlighted. One potential explanation for the observed occurrences of poor fit
is the precision limits of the power quality measurement equipment, which is specified
with measurement resolutions of $\pm 0.1\%$ and $\pm 0.01^\circ$ for harmonic currents magnitudes
and angles, respectively. Given that the tested devices are very low powered in nature,
it is reasonable to surmise that such limitations may be exercised in some instances.
Moreover, those sets of raw signal measurements, for which a small standard deviation
is observed, are also rendered more susceptible to invoking a poor fit within the corre-
sponding ZIP load models. This hypothesis is supported upon considering how all of the $R^2$ values obtained for the tested CFLs, across Tables 3 to 9, are greater than or equal to 0.97. This contrasts to the poorer fits observed among some of the LED lamps which, as Table 3 reports, have much lower power ratings than their CFL counterparts.

Another potential source of poor fit relates to the size of the phase angles measured for each harmonic current order, $\phi_h$. The error inherent in the raw measured current data propagates to the associated $\hat{a}_h$ and $\hat{b}_h$ components via calculation. The resultant error is more pronounced (in relative terms) among $\hat{a}_h$ terms for instances in which measurements of $\phi_h$ tend towards 90°, at which point large swings in $\cos(\phi_h)$ are effected by only small changes in $\phi_h$. The same problem is apparent among $\hat{b}_h$ terms, and thereby derived calculations of $\sin(\phi_h)$, for $\phi_h \simeq 0^\circ$. This issue is exacerbated when taken in combination with a small relative current magnitude, $|I_h|$, within which a comparatively high relative error will be present due to the precision limitations of the testing equipment.

Analysis of the models depicted in Figs. 6, 7 and 8 helps to yields a better sense of the problem. In Fig. 6(b), a relatively poor fit is observed for the $\hat{b}_5$ component ($R^2 = 0.45$), which arises from the small amount of variability present within the raw dataset. In terms of relative error, the condition is exacerbated by the precision limitations of the measurement equipment, such that the raw data points appear to be widely dispersed within the scatter plot representation. By contrast, an excellent fit ($R^2 = 0.97$) is depicted for the ZIP model trend of $\hat{a}_5$ in Fig. 6(a), in which a higher degree of variability is present. Upon recovery of the harmonic magnitude and phase angle values in panels (c) and (d) of Fig. 6, a highly accurate fit ($R^2 = 0.97$) is obtained for $|I_5|$, while a low $R^2$ value of 0.36 is observed for $\phi_5$. However, on inspection of the raw data points in Fig. 6(d), a very low degree of variation is witnessed, and the corresponding ZIP model is actually shown to offer a very accurate approximation. The low $R^2$ value...
offers a misleading interpretation of the model, as it arises from the very low standard deviation of the raw measured data set.

A similar prognosis is evident for LED lamp 07, where a poor fit ($R^2 = 0.25$) is observed for the $\hat{a}_h$ component in Fig. 7(a), with a highly accurate representation ($R^2 = 0.99$) being possible for $\hat{b}_h$ in Fig. 7(b). Once again, a highly accurate model for $|I_3|$ is evident in Fig. 7(c), with a poorer fit ($R^2 = 0.60$) obtained for the very small voltage induced fluctuations of $\phi_3$ in Fig. 7(d).

Similarly, it is important to note that for some cases, the inadequacies of using ZIP model theory to fit against measurement discontinuities and/or piecewise behavior becomes apparent. One such example, is contained within the ZIP models for harmonic current $I_9$, pertaining to LED lamp 07 (Fig. 8), in which a significant discontinuity is present in the measured data set, around nominal voltage. Limitations of this nature, for the ZIP modelling approach are acknowledged in literature [20, 33], but may be treated as an accepted weakness of a quadratic formula based approach. While higher order, polynomial models may offer better resolution, additional coefficients would need to be defined and natural orientation of the ZIP based approach towards an amalgam of constant impedance, current and power behaviors would be lost.

7. Conclusions and Further Work

This paper has advanced a verifiable method for characterizing the active power, reactive power and harmonic current distortion attributes of a selection of modern lighting types. Results are presented in the form of ZIP coefficients in Tables 3 to 9. These are translated into sets of characteristic curves that are plotted against applied voltage level in Figs. 3, 4 and 12 in which they may also be visually compared against the corresponding experimental measurements.

The generally poor harmonic performance of modern lamps may be thought to
detract from the otherwise excellent low power benefits imparted to customers. As the adoption of such technologies proliferates, utilities may begin to observe higher technical loss percentages within LV networks [9]. This will manifest as an increased operational cost that is not easily recoverable under present electricity market paradigms. If the findings of this research can be extrapolated to the distribution network level, it may be argued that the lowering of service level voltages, through innovations such as CVR, can assist in mitigating some of the future increases in harmonic distortion. However, one should invoke caution regarding the applicability of this research in relation to LV networks within which very high levels of $THD_V$ are present. As stated in §2.3, a comprehensive analysis can only be performed for circumstances in which a considerably more complex model for harmonic current phasor characterization is developed.

Given their apparent technical superiority; alongside other factors such as longer lifetimes and waste disposal concerns related to CFLs, one can anticipate how LED technology is likely to supplant CFLs in popularity as manufacturing costs decline. As such, it is the characteristics of the LED lamps presented in this paper that ought to be most seriously considered in the context of future network load growth. As residential uptake of LED lamps grows, load models used by utilities may require adjustment to encompass larger quotients of constant power type behavior. The emergence of LED street lighting will further accentuate this transition. Consequentially, the continuing effectiveness of network management applications, such as CVR, in indirectly reducing customer demand might be expected to decline.

Despite recent efforts to address the historically lax harmonic current restrictions for low powered luminaires, the new limits specified in [16] remain more lenient than those defined for devices with power ratings greater than 25 W. This is amidst projections that LED types will become the dominant lighting technology over the next twenty years [14]. Although such advancements are associated with higher device efficiencies,
one should not discount the potential applicability of Jevon’s Paradox to this discussion. As such, issues pertaining to higher distortion levels within LV networks may be set to emerge as customers migrate from GIL and CFL types. However, ample evidence is presented, in charts such as Figs. 9 and 10, to suggest that the greater propensity for harmonic diversity among LED lamps, rather than CFLs, should help to assuage some of these concerns about large increases in harmonic current flows within LV networks.

Several potential areas of further work emanate from the analysis presented in this paper. The developed models can be used to more comprehensively define the characteristics of connected load instances within distribution network models, in preparation for analysis with power systems load flow softwares. The characterization results, presented in Tables 3 to 9, serve as reference data in this regard. For large network models, the use of harmonic diversity factors, alongside experimental power quality data accrued for different types of consumer appliance, can be used to produce realistic, aggregate lump load instances, within which the demand attributes of a whole LV feeder (for example) may be encapsulated, as a means of reducing the complexity of the overall network model. Finally, as discussed in §2.3, the development of a more extensive modeling framework, appropriate for the resolution of harmonic currents under high background levels of $THD_V$, warrants investigation but is likely to encounter computational challenges that may detract from its intended convenience.

Acknowledgement

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CAPRICA (EP/M002837/1) and CASE DINOSAUR programs. G. McLorn thanks the Department of the Economy (Northern Ireland) for supporting his research.

8. References


[34] Keeping the lights on - an inconvenient truth, remarks by N. Burton at the IET President’s Address 2010 lecture, London (Oct. 2010).
Figures and Tables

![Experimental Apparatus - Connectivity Diagram](image)

Figure 1: Experimental Apparatus - Connectivity Diagram

<table>
<thead>
<tr>
<th>Harmonic Order ( n )</th>
<th>Max. Harmonic Current (% of ( I_1 ))</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>30 - ( \lambda )</td>
</tr>
<tr>
<td>5</td>
<td>10</td>
</tr>
<tr>
<td>7</td>
<td>7</td>
</tr>
<tr>
<td>9</td>
<td>5</td>
</tr>
<tr>
<td>11 ( \leq n ) (odd) ( \leq 39 )</td>
<td>3</td>
</tr>
</tbody>
</table>

Table 1: Harmonic Current Limits for IEC 61000-3-2/Class C devices (> 25 W) (% of Fundamental Current, \( I_1 \))

<table>
<thead>
<tr>
<th>Harmonic Order ( n )</th>
<th>Max. Harmonic Current (% of ( I_1 ))</th>
</tr>
</thead>
<tbody>
<tr>
<td>(i)</td>
<td>(ii)</td>
</tr>
<tr>
<td>3</td>
<td>78.2</td>
</tr>
<tr>
<td>5</td>
<td>43.7</td>
</tr>
<tr>
<td>7</td>
<td>23.0</td>
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<tr>
<td>9</td>
<td>11.5</td>
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<td>11</td>
<td>8.1</td>
</tr>
<tr>
<td>13 ( \leq n ) (odd) ( \leq 39 )</td>
<td>88.6/n</td>
</tr>
</tbody>
</table>

Table 2: Inferred Harmonic Current Limits for IEC 61000-3-2/Class C devices (≥ 5 W and ≤ 25 W); Nominal Voltage = 230 V (% of Fundamental Current, \( I_1 \))
Figure 2: Bulb 05. Harmonic currents in polar form (magnitude in mA, phase angle in degrees, reference current of 50 mA). Orders (a) 3, (b) 5, (c) 7, (d) 9, (e) 11, (f) 13 and (g) fundamental. Observed under application of voltages between 0.90 and 1.10 pu.

Figure 3: Per-unit active power vs. voltage; LED, CFL and GIL lamps
Figure 4: Per-unit reactive power vs. voltage; LED and CFL lamps

Figure 5: 3rd order harmonic currents (magnitude in mA, phase angle in degrees). Observed under application of voltages between 0.90 and 1.10 pu for (a) LED lamp 02 and (b) CFL lamp 09.

Table 3: ZIP models for Active and Reactive Power Consumption

<table>
<thead>
<tr>
<th>Bulb</th>
<th>Bulb Type</th>
<th>Rating (W)</th>
<th>$P_0$</th>
<th>$R_p^2$</th>
<th>$Z_p$</th>
<th>$I_p$</th>
<th>$P_p$</th>
<th>$Q_p$</th>
<th>$R_q^2$</th>
<th>$Z_q$</th>
<th>$I_q$</th>
<th>$P_q$</th>
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<tbody>
<tr>
<td>01</td>
<td>LED/Globe</td>
<td>9.0</td>
<td>9.81</td>
<td>1.00</td>
<td>-0.446</td>
<td>1.823</td>
<td>-0.377</td>
<td>-3.26</td>
<td>0.85</td>
<td>-0.447</td>
<td>1.242</td>
<td>0.205</td>
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<tr>
<td>02</td>
<td>LED/Globe</td>
<td>8.0</td>
<td>7.10</td>
<td>1.00</td>
<td>0.000</td>
<td>0.000</td>
<td>1.000</td>
<td>-2.67</td>
<td>0.98</td>
<td>1.091</td>
<td>-3.204</td>
<td>3.113</td>
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<tr>
<td>03</td>
<td>LED/Globe</td>
<td>10.0</td>
<td>9.50</td>
<td>0.81</td>
<td>0.613</td>
<td>-1.284</td>
<td>1.671</td>
<td>-2.74</td>
<td>0.99</td>
<td>-3.826</td>
<td>9.773</td>
<td>-4.947</td>
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<tr>
<td>04</td>
<td>LED/Downlight</td>
<td>5.5</td>
<td>4.21</td>
<td>0.99</td>
<td>0.623</td>
<td>-0.393</td>
<td>0.770</td>
<td>-1.38</td>
<td>0.98</td>
<td>0.634</td>
<td>0.941</td>
<td>-0.575</td>
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<td>05</td>
<td>LED/Globe</td>
<td>8.5</td>
<td>8.45</td>
<td>1.00</td>
<td>0.000</td>
<td>0.000</td>
<td>1.000</td>
<td>-3.43</td>
<td>0.99</td>
<td>2.207</td>
<td>-5.552</td>
<td>4.345</td>
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<td>06</td>
<td>LED/Globe</td>
<td>5.7</td>
<td>5.76</td>
<td>0.94</td>
<td>-0.253</td>
<td>0.814</td>
<td>0.439</td>
<td>-3.12</td>
<td>1.00</td>
<td>1.026</td>
<td>0.291</td>
<td>-0.317</td>
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<tr>
<td>07</td>
<td>LED/Downlight</td>
<td>6.0</td>
<td>5.70</td>
<td>0.87</td>
<td>-0.195</td>
<td>0.574</td>
<td>0.621</td>
<td>-1.98</td>
<td>0.98</td>
<td>2.068</td>
<td>-2.658</td>
<td>1.589</td>
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<tr>
<td>08</td>
<td>CFL/Spiral</td>
<td>20.0</td>
<td>17.80</td>
<td>1.00</td>
<td>1.124</td>
<td>-1.135</td>
<td>1.011</td>
<td>-10.31</td>
<td>1.00</td>
<td>1.386</td>
<td>-2.131</td>
<td>1.746</td>
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<tr>
<td>09</td>
<td>CFL/Triple Tube</td>
<td>11.0</td>
<td>10.70</td>
<td>1.00</td>
<td>0.000</td>
<td>1.121</td>
<td>-0.121</td>
<td>-5.79</td>
<td>1.00</td>
<td>0.987</td>
<td>-1.214</td>
<td>1.227</td>
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<td>10</td>
<td>CFL/Globe</td>
<td>9.0</td>
<td>8.03</td>
<td>1.00</td>
<td>0.356</td>
<td>0.708</td>
<td>-0.064</td>
<td>-4.13</td>
<td>1.00</td>
<td>0.693</td>
<td>-0.270</td>
<td>0.578</td>
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<td>GIL</td>
<td>60.0</td>
<td>56.41</td>
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<td>-0.575</td>
<td>0.511</td>
<td>-0.34</td>
<td>0.85</td>
<td>4.290</td>
<td>-5.233</td>
<td>1.944</td>
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</tbody>
</table>

31
Cartesian cosine component ($R^2 = 0.97$) 

Cartesian sine component ($R^2 = 0.45$) 

(a) 

(b) 

Resultant current phasor magnitude ($R^2 = 0.97$) 

Resultant current phasor angle ($R^2 = 0.36$) 

(c) 

(d) 

Figure 6: LED lamp 04. Measured values and ZIP fitted trends for 5th harmonic current vs. voltage: 
(a) $|\hat{I}_5| \cdot \cos \hat{\phi}_5$ component, 
(b) $|\hat{I}_5| \cdot \sin \hat{\phi}_5$ component, 
(c) Phasor magnitude, $|\hat{I}_5|$ (%), 
and (d) Phasor phase angle, $\angle \hat{\phi}_5$ (°).

Table 4: ZIP model representations of 3rd order harmonic current, as % of Fundamental

<table>
<thead>
<tr>
<th>Bulb</th>
<th>Bulb Type</th>
<th>$\alpha_{3,5}(%)$</th>
<th>$R_{2,5}$</th>
<th>$Z_{2,5}$</th>
<th>$I_{2,5}$</th>
<th>$P_{2,5}$</th>
<th>$\beta_{3,5}(%)$</th>
<th>$R_{2,5}$</th>
<th>$Z_{2,5}$</th>
<th>$I_{2,5}$</th>
<th>$P_{2,5}$</th>
<th>$\delta_{3,5}(%)$</th>
<th>$\angle I_{3,5}$ (°)</th>
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<td>01</td>
<td>LED/Globe</td>
<td>-51.1</td>
<td>1.00</td>
<td>-0.358</td>
<td>1.668</td>
<td>-0.310</td>
<td>-74.6</td>
<td>1.00</td>
<td>-0.200</td>
<td>0.136</td>
<td>1.065</td>
<td>90.4</td>
<td>-124.4</td>
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<td>02</td>
<td>LED/Globe</td>
<td>-41.8</td>
<td>1.00</td>
<td>-2.645</td>
<td>7.699</td>
<td>-3.963</td>
<td>-79.1</td>
<td>1.00</td>
<td>-0.504</td>
<td>0.672</td>
<td>0.832</td>
<td>89.5</td>
<td>-117.8</td>
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<tr>
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<td>0.99</td>
<td>1.866</td>
<td>-5.074</td>
<td>4.207</td>
<td>-1.4</td>
<td>0.97</td>
<td>87.300</td>
<td>-167.46</td>
<td>81.613</td>
<td>8.0</td>
<td>-10.3</td>
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<td>37.1</td>
<td>0.97</td>
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<td>-1.936</td>
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<td>1.00</td>
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<td>-80.9</td>
<td>1.00</td>
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<td>1.00</td>
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<td>1.00</td>
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<td>1.00</td>
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<td>11</td>
<td>GIL</td>
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<td>0.378</td>
<td>0.875</td>
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<td>0.992</td>
<td>1.1</td>
<td>-83.7</td>
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Figure 7: LED lamp 07. Measured values and ZIP fitted trends for 3rd harmonic current vs. voltage: (a) $|\hat{I}_3| \cdot \cos \hat{\phi}_3$ component, (b) $|\hat{I}_3| \cdot \sin \hat{\phi}_3$ component, (c) Phasor magnitude, $|\hat{I}_3|$ (%), and (d) Phasor phase angle, $\angle \hat{\phi}_3$ (°).

Table 5: ZIP model representations of 5th order harmonic current, as % of Fundamental

| Bulb Type | Bulb Type | $a_5 \equiv |\hat{I}_5| \cdot \cos \hat{\phi}_5$ | $b_5 \equiv |\hat{I}_5| \cdot \sin \hat{\phi}_5$ | $|I_0|$ (%) | $\angle I_5$ (°) |
|-----------|-----------|------------------------------------------|------------------------------------------|----------|----------------|
| 01 LED/Globe | -5.9 | 1.00 | -0.059 | -8.221 | 11.279 | 73.7 | 1.00 | -0.790 | 2.046 | -0.256 | 73.9 | 94.5 |
| 02 LED/Globe | -19.6 | 1.00 | -0.289 | -5.510 | 6.799 | 68.9 | 1.00 | -3.183 | 7.681 | -3.498 | 71.6 | 105.9 |
| 03 LED/Globe | -0.13 | 0.28 | -223.35 | 438.53 | -214.18 | -0.05 | 0.97 | -3.183 | 7.681 | -3.498 | 71.6 | 105.9 |
| 04 LED/Downlight | 22.6 | 0.97 | -0.901 | 1.945 | -0.014 | -5.8 | 0.45 | -1.270 | 2.568 | -0.298 | 23.3 | -44.4 |
| 05 LED/Globe | -29.0 | 1.00 | -1.866 | 0.023 | 2.843 | 60.6 | 1.00 | -4.426 | 10.865 | -5.439 | 67.2 | 115.6 |
| 06 LED/Globe | 19.9 | 1.00 | -4.276 | 10.856 | -5.580 | -0.05 | 0.97 | -3.183 | 7.681 | -3.498 | 71.6 | 105.9 |
| 07 LED/Downlight | 1.8 | 0.99 | 8.428 | -13.268 | 5.840 | -8.1 | 0.97 | 0.546 | 0.979 | -0.526 | 8.3 | -77.6 |
| 08 CFL/Spiral | -39.2 | 0.99 | -1.748 | 3.643 | -0.895 | 18.9 | 1.00 | -7.787 | 20.040 | -11.222 | 43.6 | 154.2 |
| 09 CFL/Triple Tube | -37.2 | 1.00 | -0.520 | 0.482 | 1.038 | 37.4 | 1.00 | -2.245 | 6.459 | -3.214 | 52.8 | 134.8 |
| 10 CFL/Globe | -42.0 | 1.00 | -0.500 | 0.940 | 0.560 | 28.4 | 1.00 | -2.180 | 6.115 | -2.934 | 50.7 | 145.9 |
| 11 GIL | 0.0 | - | 0.000 | 0.000 | 1.000 | 0.0 | - | 0.000 | 0.000 | 1.000 | 0.0 | 45.0 |
Cartesian cosine component ($R^2 = 0.90$)  
Cartesian sine component ($R^2 = 0.18$)  
(a) (b)  
Resultant current phasor magnitude ($R^2 = 0.15$)  
Resultant current phasor angle ($R^2 = 0.95$)  
(c) (d)  

Figure 8: LED lamp 07. Measured values and ZIP fitted trends for 9th harmonic current vs. voltage: (a) $|\hat{I}_9| \cdot \cos \hat{\phi}_9$ component, (b) $|\hat{I}_9| \cdot \sin \hat{\phi}_9$ component, (c) Phasor magnitude, $|\hat{I}_9|$ (%), and (d) Phasor phase angle, $\angle \hat{\phi}_9$ (°).

Table 6: ZIP model representations of 7th order harmonic current, as % of Fundamental
Figure 9: Mean MDF for harmonic current orders 1, 3, 5, 7, 9, 11 and 13, for number of device instances $N \leq 100$. Homogeneous collections of (a) LED and (b) CFL instances, by lamp type and harmonic current order

Figure 10: Mean MDF for harmonic current orders 1, 3, 5, 7, 9, 11 and 13, for number of device instances $N \leq 100$. Mixed lamp type combinations of (a) LED and (b) CFL instances, by harmonic current order
Figure 11: Mean ADF for harmonic current orders 1, 3, 5, 7, 9, 11 and 13, for number of device instances $N \leq 100$. Mixed lamp type combinations of (a) LED and (b) CFL instances, by harmonic current order.

Figure 12: Per-unit total non-fundamental harmonic current vs. voltage; LED, CFL and GIL lamps.
Table 7: ZIP model representations of $9^{th}$ order harmonic current, as % of Fundamental

| Bulb Type       | $a_h = |I_h| \cdot \cos \phi_h$ | $b_h = |I_h| \cdot \sin \phi_h$ |
|-----------------|---------------------------------|---------------------------------|
| 01 LED/Globe    | -35.2                            | -13.4                           |
| 02 LED/Globe    | -27.2                            | -17.7                           |
| 03 LED/Globe    | 4.0                              | 0.3                             |
| 04 LED/Downlight| 8.4                              | -4.4                            |
| 05 LED/Globe    | -13.4                            | -24.5                           |
| 06 LED/Globe    | -8.4                             | -6.3                            |
| 07 LED/Downlight| 0.4                              | -0.18                           |
| 08 CFL/Spiral   | 20.5                             | 0.0                             |
| 09 CFL/Triple Tube | 12.9                       | -15.9                          |
| 10 CFL/Globe    | 18.7                             | -8.6                            |
| 11 GIL          | 0.0                              | 0.0                             |

Table 8: ZIP model representations of $11^{th}$ order harmonic current, as % of Fundamental

| Bulb Type       | $a_h = |I_h| \cdot \cos \phi_h$ | $b_h = |I_h| \cdot \sin \phi_h$ |
|-----------------|---------------------------------|---------------------------------|
| 01 LED/Globe    | 7.0                             | 22.6                           |
| 02 LED/Globe    | -6.7                            | 21.6                           |
| 03 LED/Globe    | 3.5                             | -0.2                           |
| 04 LED/Downlight| 4.8                             | -3.3                           |
| 05 LED/Globe    | -18.5                           | 13.2                           |
| 06 LED/Globe    | -10.1                           | -3.9                           |
| 07 LED/Downlight| -0.5                            | -3.3                           |
| 08 CFL/Spiral   | -0.9                            | -17.9                          |
| 09 CFL/Triple Tube | -16.8                      | -11.6                          |
| 10 CFL/Globe    | -9.3                            | -17.3                          |
| 11 GIL          | 0.0                             | 0.0                            |

Table 9: ZIP model representations of $13^{th}$ order harmonic current, as % of Fundamental

| Bulb Type       | $a_h = |I_h| \cdot \cos \phi_h$ | $b_h = |I_h| \cdot \sin \phi_h$ |
|-----------------|---------------------------------|---------------------------------|
| 01 LED/Globe    | 16.7                            | -12.0                           |
| 02 LED/Globe    | 21.2                            | 0.7                            |
| 03 LED/Globe    | 3.1                             | -0.4                           |
| 04 LED/Downlight| 2.3                             | -2.1                           |
| 05 LED/Globe    | 18.0                            | 13.8                           |
| 06 LED/Downlight| -7.5                            | 2.8                            |
| 07 LED/Downlight| -1.4                            | -1.9                           |
| 08 CFL/Spiral   | -13.2                           | -1.2                           |
| 09 CFL/Triple Tube | -6.4                         | 15.7                           |
| 10 CFL/Globe    | -12.6                           | 8.1                            |
| 11 GIL          | 0.0                             | 0.0                            |

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