UWB Antenna with Dynamically Reconfigurable Notch-Band using Rectenna and Active Booster

Abdul Quddious1, Muhammad Ali Bahar Abbasi1, Farooq A. Tahir2, M.A. Antoniades3, Photos Vryonides1 and Symeon Nikolau1

1Department of Electrical Engineering, Frederick University, 7, Y. Frederickou Str. Pallouriotisa, Nicosia, Cyprus
2Research Institute for Microwave and Millimeter-Wave Studies (RIMMS), National University of Sciences & Technology (NUST), Sector H-12, Islamabad, Pakistan
3Department of Electrical and Computer Engineering, University of Cyprus, 1 Panepistimiou Avenue 2109 Aglantzia, Nicosia, Cyprus
12mseaqquddious@seecs.edu.pk

Abstract: In this paper a UWB monopole antenna with a dynamically reconfigurable notch-band, along with the associated RF-triggered power management unit (PMU) that enables the dynamic reconfigurability, are presented. The UWB monopole antenna has a rectangular slot which hosts a J-shaped stub, electrically connected with the radiator using one PIN diode switch. When the diode is OFF, the monopole antenna has a UWB characteristic, while when it is turned ON a frequency notch is created at 5.6 GHz. The diode is fed from a PMU that consists of a rectenna and a DC-to-DC active booster. When the rectenna that consists of a patch antenna and a rectifier receives a -11 dBm or higher 5.6 GHz signal, it rectifies the RF signal into a sufficiently high DC voltage. The rectified DC voltage is applied to the cascaded DC-to-DC booster as enabling signal, and the booster provides at its output terminal sufficient DC power to actuate the PIN diode and thus dynamically reconfigure the UWB antenna. The dynamically reconfigurable notch-band is created immediately in response to the reception of an external, -11 dBm RF signal at 5.6 GHz, and it disappears in real time immediately after the external RF signal is removed.

1. Introduction

Ultra-wideband (UWB) devices are widely used in wireless sensor networks, healthcare and biomedical wireless systems, and in-house devices in radar detecting, locating, and communications [1]. Presently, the increasing numbers of narrow band applications that share part of the UWB spectrum are causing heavy congestion in the UWB spectrum causing interference and degrading the systems’ performance [2]. These applications include 3.6 GHz IEEE 802.11y Wireless Local Area Networks (WLAN) (3.6575–3.69 GHz), 4.9 GHz public safety WLAN (4.94–4.99 GHz) and 5 GHz IEEE 802.11a/h/j/n WLAN (5.15–5.35 GHz, 5.25–5.35 GHz, 5.47–5.725 GHz, 5.725–5.825 GHz) and they all operate within the FCC UWB band of 3.1-10.6 GHz. To overcome this problem, agile radios are required that make use of smart reconfigurable UWB antennas capable of cancelling single-, dual- or multi-band interference [3-9]. There are several methods to achieve a band-notched UWB antenna. U-shaped slots, L-shaped slots [5], or capacitive-loaded loops (CLL) [6, 7] can be added either on the radiating element or on the RF ground plane. Alternatively, λ/4 open stubs or λ/2 parasitic linear segments can be added following the approach used in [3-6].

To achieve the band-notch reconfigurability, electrical switches are used to connect and disconnect antenna parts in order to redistribute the antenna surface currents. Many designs have used PIN diodes [7, 10] to switch between “ON”/“OFF” states, or varactors [3] using continuously tunable voltage elements. Alternatively, radio-frequency micro-electromechanical systems (RF-MEMS) [5] have been used to reconfigure antenna characteristics. Comparing PIN diodes and RF-MEMS someone should consider that the switching time of RF-MEMS is in the range of 1–200 µsec.
which may be considered slow for some applications [11]. PIN diodes or varactors have appeared to be a faster and a more compact alternative to RF-MEMS. The switching time of a PIN diode is in the range of 1–100 nsec. Moreover, the biasing voltage for the RF-MEMS and varactors is very high (10–100 V) as compared to PIN diodes (0.3–0.9 V). Biasing voltage is more critical for the required dynamical actuation presented in this work therefore a PIN diode is used as a switch.

Recently, there has been significant research effort towards wireless power transfer (WPT) applications. More often than not, the harvested power is insufficient to support autonomous operation. However, the rectified RF signal can be utilized as an enabling voltage in order to control an active DC-DC power booster to supply in turn the required power. For increasing the sensitivity, which means effective sensing of a low power RF signal, it is necessary to develop a high-efficiency rectenna (antenna + rectifier) for a wide dynamic range of the available power levels. During the past decade considerable research effort has focused on high-efficiency rectifiers for both low power, [12-15] and high power [16, 17] RF energy rectification. Since the rectifier is non-linearly dependent on the terminating load resistance [16], a power management unit (PMU) is added in order to provide the optimum termination load for the rectifier. Some of the power management units, presented in the literature, aim to effectively store the harvested energy in a rechargeable battery [18, 19].

In the proposed dynamically reconfigurable UWB antenna shown schematically in Fig. 1(a), the associated rectenna uses the rectified RF energy to drive the DC-DC power booster, since through an enabling voltage the output voltage from the booster is applied to the PIN diode, setting up a power PMU from which the PIN diode is biased to reconfigure the UWB antenna dynamically. Both the UWB antenna and the rectenna are fabricated using microstrip technology.

### Table 1 Schematic Dimensions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>W</th>
<th>L</th>
<th>l1</th>
<th>l2</th>
<th>l3</th>
<th>l4</th>
<th>l5</th>
<th>l6</th>
<th>l7</th>
<th>l8</th>
<th>l9</th>
<th>Value (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>UWB Antenna</td>
<td>24.5</td>
<td>35.0</td>
<td>14.0</td>
<td>6.75</td>
<td>5.74</td>
<td>1.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PIN</td>
<td>l1</td>
<td>l6</td>
<td>l7</td>
<td>l8</td>
<td>l9</td>
<td>W1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Value (mm)</td>
<td>1.4</td>
<td>1.71</td>
<td>13.0</td>
<td>6.10</td>
<td>12.1</td>
<td>12.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PIN</td>
<td>W2</td>
<td>W3</td>
<td>r1</td>
<td>r2</td>
<td>r3</td>
<td>θ1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Value (mm)</td>
<td>2.40</td>
<td>12.5</td>
<td>8.00</td>
<td>6.00</td>
<td>90°</td>
<td>90°</td>
<td></td>
<td></td>
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<thead>
<tr>
<th>Parameter</th>
<th>l1</th>
<th>l2</th>
<th>l3</th>
<th>l4</th>
<th>l5</th>
<th>l6</th>
<th>l7</th>
<th>l8</th>
<th>l9</th>
<th>ll</th>
<th>Value (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rectenna</td>
<td>17.6</td>
<td>9.0</td>
<td>6.1</td>
<td>5.23</td>
<td>4.50</td>
<td>5.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PIN</td>
<td>l7</td>
<td>l8</td>
<td>w1</td>
<td>w2</td>
<td>w3</td>
<td>w4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Value (mm)</td>
<td>4.0</td>
<td>11.4</td>
<td>21.2</td>
<td>0.3</td>
<td>2.5</td>
<td>3.00</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PIN</td>
<td>w5</td>
<td>w6</td>
<td>w7</td>
<td>θ3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Value (mm)</td>
<td>0.6</td>
<td>2.40</td>
<td>1.32</td>
<td>35°</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
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2. UWB Antenna With Reconfigurable Band-Notch

The geometry of the UWB monopole antenna with a rectangular slot is shown in Figs 1(b) and 1(c). The UWB antenna was fabricated on a 0.787 mm thick Rogers RT/duriod 5880 substrate with relative permittivity εr = 2.2 and loss tangent tanδ = 0.0009, and overall board dimensions of 35 (L) × 24.5 (W) mm².

The rectangular monopole was fed by a 50 Ω characteristic impedance microstrip line, 2.4 mm wide, and 13 mm long. As depicted in Fig. 1, the SMA connector was included in the simulation model to improve the agreement between the simulated results and the measurements on the fabricated prototype antenna. Both the radiating patch and the RF ground plane were chamfered at the bottom and top edges respectively. These chamfered edges produce a smooth transition from the microstrip line guided modes to the radiated ones and they contribute towards the matching improvement over the UWB band [3, 4]. The improvement can be verified in Fig. 2 where the effect of the chamfered corners is presented with the red dotted line.

The original model used typical rectangular patches with 90-degree corners for both the monopole radiator, and the ground plane (black solid line). A rectangular slot on the radiating monopole also contributes towards achieving even better impedance matching over the entire UWB frequency range. Fig. 2 displays the effect on the impedance matching of the designed UWB antenna when the corners are rounded and when a rectangular slot is added to the monopole radiator. The comparison of [b] is shown for the three different versions of the initial design stage of the proposed UWB antenna. It can be observed that the combination of the rectangular slot with the rounded edges improves the impedance matching significantly. More importantly, the space created by the rectangular slot can be used to accommodate a stub that is subsequently used to create a band-notch response. The fabricated prototype is depicted as inset in Fig. 2.

The J-shaped stub is added inside the created rectangular slot, as shown in Fig. 1(b) and can be clearly recognized in Fig. 3. The stub can create a bandstop filter response (band-notch) in the frequency range of 5-6 GHz, where a number of narrowband applications exist: HIPERLAN2 (5.470–5.725 GHz) and 5 GHz IEEE 802.11a/h/j/i WLAN (5.15–5.35 GHz, 5.25–5.35 GHz, 5.47–
5.725 GHz and 5.725–5.825 GHz). The purpose is to eliminate the potentially interfering signal which can be considered as noise for the UWB communications system, and consequently improve the received SNR of the UWB receiver [5]. The bandstop filter characteristic can be dynamically added to the conventional UWB antenna, in the presence of an interferer signal higher than -10 dBm. The detailed simulation model of the dynamically reconfigurable UWB antenna is illustrated in Fig. 1, and the optimized dimensions of the fabricated antenna are summarized in Table 1. In order to accommodate the addition of a switch to implement the dynamic reconfigurability, a gap with a length of 1.2 mm is created along the stub. To achieve electronic switching, a silicon PIN diode by Skyworks (model SMP1345-079LF) was used, which is specifically suitable for WLAN applications. Simulations were carried out with the CST Microwave Studio and Design Studio co-simulations with PIN diodes’ s-parameters (.s2p) files provided by the Skyworks [20] for different forward and reverse biasing conditions.

For the biasing network, small square pads of 1 × 1 mm² are introduced to connect to DC lines via chip inductors (82 nH). These inductors were used as RF chokes to reduce the leakage of RF energy. The DC lines are connected to the biasing circuit on the backside of the antenna through vias to prevent the DC wires from perturbing the radiation performance of the antenna. The biasing DC voltage is provided from the output of the DC-to-DC power booster that is subsequently discussed in Section 4. The square pads and the DC wires can be seen in Figs 1(b) and 1(c), and also in the photograph of the fabricated reconfigurable band-notch UWB antenna in the inset of Fig. 2. The square pads and the DC lines are connected to the biasing circuit on the backside of the antenna through vias to prevent the DC wires from perturbing the radiation performance of the antenna. The biasing DC voltage is controlled by the geometric characteristics of the stub (length, width and position). The stub parameters can be modified to demonstrate a practical implementation of the presented antenna concept when it is required to reject signals from the 5.0 GHz IEEE 802.11y HIPERLAN/2 band (5.470–5.725 GHz) and the 5 GHz IEEE 802.11a/h/j/n WLAN band (5.15–5.35 GHz, 5.25–5.35 GHz, 5.47–5.725 GHz and 5.725–5.825 GHz). To estimate the required stub length for defined specifications which is independent of the substrate dielectric constant, the following piece-wise analytical expression for the stub length \( L_{\text{stub}} \) was derived, by curve fitting the full-wave simulation results:

\[
L_{\text{stub}} = -4.852 f_0^3 + 77.97 f_0^2 - 421.2 f_0 + 774.1
\]

Where \( L_{\text{stub}} \) is the stub length in mm and \( f_0 \) is the notch’s central frequency in GHz. In order to reject interference caused by IEEE 802.11j (5.47–5.725 GHz) systems, the initial stub length was estimated, using (1), for the central
frequency of 5.6 GHz. Then the overall stub dimensions (length and width) were fine-tuned using CST full-wave simulations to maximize the peak at the central frequency. The final dimensions for the optimized stub are \( L_{\text{stub}} = 8.65 \text{ mm} \) and \( w = 0.5 \text{ mm} \).

3. UWB Antenna Measurement Results

The \(|S_{11}|\) comparison between the simulated, and measured results of the proposed UWB antenna with dynamically reconfigurable band-notch are shown in Fig. 4. In the “OFF” state of the diode, a typical UWB antenna response is observed. When the diode is switched to the “ON” state, a frequency notch appears between 5 and 6 GHz with a center frequency of 5.6 GHz. Fig. 4b, depicts the simulated and measured VSWR when the dynamically reconfigurable band-notch is created. The S-parameter measurements were taken using an Agilent E8363B vector network analyzer (VNA) and good agreement is observed between the simulated and measured results. The diode can be turned “ON” in the presence of an RF interfering signal as low as -10 dBm sensed and rectified by the associated rectenna, as is subsequently demonstrated in Section 5. The radiation pattern measurements were taken in an anechoic chamber at the Research Institute for Microwave and Millimeter-wave Studies (RIMMS), co-owned by the National University of Sciences and Technology (NUST). The antenna was placed on a rotating platform and gain patterns were measured at discrete frequency points. The gain of the antenna was measured using the substitution method in which a standard rectangular horn antenna of known gain was used.

The measured and the simulated far field radiation patterns at 4 and 8 GHz are shown in Fig. 5 and Fig. 6 when the diode is in the “ON” and “OFF” states, respectively. As expected from the antenna geometry, the E-plane (x-z plane) patterns are basically monopole-like in all cases, while the H-plane (y-z plane) patterns are almost omni-directional. Fig. 7 provides the antenna peak realized gain values versus the operation frequency. It is observed that the simulated and measured gain values significantly decrease at the band-notched frequency of 5.6 GHz while they remain mostly unchanged everywhere else.

4. RF Triggered Power Management Unit

For the dynamic biasing of the PIN diode an RF scavenging system is needed. Since the required power for the biasing for the PIN diode is 9 mW (9.54 dBm), which is higher than the usually available RF power, an active RF-triggered power management unit is used. The RF triggered PMU system consists of a rectenna to convert a 5.6 GHz RF incident signal into a DC voltage and a DC-to-DC power booster that uses the rectified voltage as an enabling signal to provide - in turn – double the biasing DC voltage, to the PIN diode, and thus actuate it from “OFF” to “ON” state to dynamically and effectively reconfigure the UWB antenna.

4.1. Rectenna

The 5.6 GHz rectenna consists of a patch antenna and a voltage doubler rectifier as shown in Fig. 1(d). A well-known topology of an inset microstrip-line-fed rectangular patch antenna [21] with radiation efficiency of 97\% and realized gain of 7.6 dBi is used. Fig. 8 shows the simulated
and measured reflection coefficient of the antenna, while the inset shows a photograph of the prototype fabricated independently to test the antenna’s performance before it was directly connected to the rectifier to form the rectenna. Rectifiers operating at 5.6 GHz are not very common in the literature, since rectifiers are mostly used for the UHF and ISM bands [22]. As every other rectifier, at any frequency, it is non-linearly dependent on the input power and the termination load. When any of these quantities vary, as is the case for the cascaded DC-to-DC boost converter that has input impedance which depends on its biasing conditions, the design of a high efficiency rectifier becomes a challenging task. To maintain the cost of the rectifier low and the RF input, the rectifier prototype was first fabricated to test its performance independently. The fabricated prototype is shown in the inset photograph. A Rogers RT/duroid 5880 substrate was used with εr = 2.2, tanδ = 0.0009 and substrate thickness 0.787 mm. In the simulations, large scale signal analysis was used to analyze the behavior of the rectifier topology. It is well-known, that the rectifier has a non-linear response to the input RF signal. Therefore, a harmonic balance simulation was used in ADS, where the fundamental frequency was set to 5.6 GHz and the order number was set to 5. A power sweep from -40 dBm to +10 dBm was applied in the simulation. The RF-to-DC efficiency was optimized for a termination load of 5.1 kΩ. The fabricated prototype was first fabricated to test its performance independently. The use of the rectifier is the bandwidth of the matching network, therefore in order to increase the bandwidth the linear open stubs were replaced with radial stubs. The simulations of this final design presented in Fig. 9 were the result of a Quasi-Newton algorithm-based multivariable optimization. The geometric parameters of the rectifier along with the rectifier capacitor values were defined as variables.

The optimized values for both C1 = 50 pF and C2 = 150 pF (see Fig. 1(d)). Fig. 9 shows the comparison between the simulated and measured S11, while the fabricated prototype is displayed in the inset image. A stand-alone rectifier prototype was first fabricated to test its performance before combining it with the patch antenna. The measured S11 remains below -10 dB from 5.45 to 5.70 GHz (250 MHz band).
Fig. 12. Simulated rectifier efficiency versus output load for multiple input power levels from -25 to 0 dBm at 5.6 GHz.

Table 2 Performance Comparison of Different Rectifiers

<table>
<thead>
<tr>
<th>Ref.</th>
<th>Freq (GHz)</th>
<th>$P_{in}$ (dBm)</th>
<th>Efficiency</th>
<th>Size (cm$^2$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>This work</td>
<td>5.6</td>
<td>-10</td>
<td>43%</td>
<td>2.0 × 1.8</td>
</tr>
<tr>
<td>[24]</td>
<td>5.6</td>
<td>16.5</td>
<td>52%</td>
<td>7.2 × 4.2</td>
</tr>
<tr>
<td>[25]</td>
<td>5.4</td>
<td>50.5%</td>
<td>3.5 × 6</td>
<td></td>
</tr>
<tr>
<td>[26]</td>
<td>5.8</td>
<td>-10.1</td>
<td>39.2%</td>
<td>4.5 × 3.0</td>
</tr>
<tr>
<td>[27]</td>
<td>5.8</td>
<td>8</td>
<td>69.4%</td>
<td>4.0 × 4.0</td>
</tr>
<tr>
<td>[28]</td>
<td>5.8</td>
<td>-10.0</td>
<td>52.0%</td>
<td>-</td>
</tr>
<tr>
<td>[28]</td>
<td>5.8</td>
<td>0</td>
<td>49.2%</td>
<td>5 × 2</td>
</tr>
</tbody>
</table>

Fig. 13. Measurement setup for the rectenna. (a) Step 1 to define the input RF power in the rectifier. (b) Step 2 to calculate the rectified DC power. Rectenna and DC-to-DC boost converter measurement with signal generator.

bandwidth). To measure the efficiency of the rectifier versus the input power, the rectifier was directly connected to a signal generator (R&S® SMF100A) while the DC voltage across $R_L$ was noted at different input power levels (Pin), at 5.6 GHz. An unmodulated sine wave was used to test the efficiency of the rectifier. The efficiency was calculated by changing the power level of the signal generator from -40 dBm to +10 dBm. The simulated voltage across the termination load ($R_L = 5.1 \, \Omega$) is compared with the measurements in Fig. 10. The measured efficiency of the rectifier was calculated using Eq. (3) at the corresponding input power levels, and it was compared to the simulated predictions as shown in Fig. 10. The efficiency of the rectifier is approximately 20% at -20 dBm and increases to 44.5% at -5 dBm. Since the resulting rectified voltage and DC power were not sufficient to bias the active Schottky diode, an active DC-DC booster was needed. The rectifier has been tested using a single tone signal at 5.6 GHz to ensure fair comparison with the referenced rectifier designs summarized in Table 2. It can be observed that the proposed rectifier has generally high efficiency at low input power (43% at -10 dBm) and the most compact size compared with other recently reported designs.

4.2. Active DC-to-DC Power Booster

According to the PIN diode’s datasheet (Skyworks SMP1345) [20], around 0.89 V and 10 mA current are needed, in order to switch the diode from the “OFF” to the “ON” state. In realistic scenarios, the available RF signals do not have sufficient power to directly actuate in real time the PIN diode. In such a scenario the PIN diode should be fed directly from the harvested DC power, derived at the rectifier’s output terminal. To overcome this problem, an active DC-to-DC power booster is used to provide the required DC power for the effective biasing of the PIN diode. The proposed boost converter converts the DC power level at the expense of DC biasing voltage $V_C$ between 0.9 and 1.8 V is needed for the active booster’s normal operation. A charge pump IC (model TPS60300 from Texas Instruments) [30] is used to design the DC-to-DC converter where the rectified voltage from the rectifier’s output terminal is used as the input trigger signal at the EN terminal in order to enable the IC. When a voltage $V_{EN}$ is high enough to be used as an enabler, an output voltage ($V_{OUT1}$) that is twice the voltage at $V_{CC}$ and up to 40 mA current is delivered to the output terminal of the IC (OUT1). Five 1-µF capacitors were connected in the topology shown in Fig. 11 to implement an efficient DC-to-DC power booster. $C_{IN}$, $C_{OUT1}$, and $C_{OUT2}$ are the filter capacitors that bypass any noise or pulse to the ground while $C_D$ and $C_R$ are the flying capacitors. It should be clarified that the measured input impedance of the booster (i.e. rectifier’s $R_L$, measured between the EN and GND terminals of the IC) fluctuates between 4205 and 6700 Ω in response of the variation of $V_{CC}$ within the range of 0.0-1.8 V. The $V_{CC}$ for the set of experiments was set to 0.9V, the minimum allowed to ensure minimum power consumption. When the terminal $V_{CC}$ is set to “high”, the booster enters a DC start-up mode in which the flying capacitor $C_{OUT1}$ charges up to a maximum of $V_{CC}$, then $V_{OUT1}$ delivers twice the $V_{CC}$ with a 40 mA current. The device is enabled when $V_{EN}$ crosses the threshold from a logic “low” (< 0.5×$V_{CC}$) to a logic “high” (>0.5×$V_{CC}$). The typical start-up time for the proposed active booster model is ~400 µs. Fig. 10 shows that the efficiency of the proposed rectifier for power levels close to its sensitivity (-11 dBm) remains around its maximum value.

4.3. Power Management Unit

The efficiency of the rectifier is non-linearly dependent on the load resistance $R_L$ and the impedance of the boost converter depends on the enabling voltage $V_{EN}$. Before cascading the rectifier with the active booster, the effect on the rectifier efficiency when $R_L$ changes had to be investigated. An analysis is presented in Fig. 12, in which $R_L$ was varied from 100 Ω to 1 MΩ and the simulated efficiency is plotted for multiple input power levels. The results indicate that within the range of 4205 - 6700 Ω, which is the actual range for the measured $R_L$ values (i.e. the input impedance of the DC-to-DC power booster), the rectifier operates within its maximum efficiency region. This ensures that the RF triggered PMU successfully operates at considerably varying...
RL values for a wide range of input power levels, despite the rectifier’s non-linear dependence on the output load resistance $R_L$, as shown in equation (3). The non-linear dependence of the rectifier’s RF-DC efficiency with respect to the termination load prevents the direct connection of the rectifier with the PIN diode and makes the intermediate DC-to-DC power booster necessary despite the inevitable, total efficiency, degradation.

5. Implementation and Testing

The UWB antenna and the rectenna were fabricated on a Rogers RT/duriod 5880 substrate using an LPKF ProtoMat H100 milling machine. The PIN diodes (Skyworks SMP1345-079LF), and RF choke inductors (Coilcraft series 0402CS - 82 uH) were soldered on the top layer of the UWB antenna, while the Schottky diodes (Skyworks SMS7630-079LF) and SMD capacitors (Murata series GIM03) were mounted on the rectenna module. Finally, the charge pump IC (TI model TPS60300) with five capacitors (1 μF) was implemented on a breadboard. Wires were used to connect the rectifier output terminal to the DC-to-DC power booster. Finally, the OUT1 terminal of the converter was connected to the biasing pads on the back side of the UWB antenna using bond wires.

The measurement setup was implemented as shown in Figs 13(a) and 13(b). In Step 1 the received RF power ($P_{Rx}$) was measured using a spectrum analyzer, and in Step 2 the rectified available power at the same distance $d$, on the terminating load resistance was measured using a digital multi-meter (DMM). The patch antenna presented in Fig. 8 was connected to the signal generator to be used as an RF power transmitter and the rectenna was used at the receiving end. The generated power from the signal generator is labeled in Fig. 14 as $P_{out}$ and the received power at the input of the rectifier is labeled as $P_{Rx}$. The DC-to-DC power booster circuit, was connected to the output terminal of the rectifier in order to use the rectified voltage as an enabling voltage. During the measurement process, it was observed that when the output voltage of the rectifier (which is also the driving voltage $V_{DC}$ of the DC-to-DC power booster) went above 0.43 V, the power booster successfully actuated the PIN diode that was used on the UWB antenna. When $V_{DC}$ dropped below the threshold, the output voltage of the charge pump IC was disabled and the PIN diode returned to the “OFF” state in real time.

In order to verify the successful implementation and the performance of the proposed UWB antenna with the dynamically reconfigurable notch-band, S-parameter measurements of the UWB antenna (Fig. 2) were taken in both the absence and presence of the RF triggering signal. In the laboratory setup presented in Fig. 13(b), RF signals of multiple power levels were generated by the signal generator that was connected to a patch antenna (identical to the one in Fig. 8 inset) acting as a transmitter. The transmitted, unmodulated, 5.6 GHz signal, was received and rectified from the rectenna. The rectified RF signal was used as an enabling voltage for the cascaded active booster, to make its power supply available at its terminal OUT1, in order to dynamically actuate the PIN diode and thus reconfigure the UWB antenna. The output voltage of the boost converter was measured with a DMM. Testing was repeated for three different distances between the transmitter and the receiver antennas, and three different power levels and the measurement results are summarized in Fig. 14. The actuation of the PIN diode was successful when the received power $P_{Rx}$ at the input of the rectifier was higher than -10 dBm.

Dynamic reconfiguration of the UWB antenna was observed at distances between the transmitter and receiver antennas of 0.5, 0.15, and 0.25 m when the signal generator power (Pout) was set to 0, 5 and 10 dBm, respectively. According to FCC the maximum transmitted power of Unlicensed National Information Infrastructure (U-NII) Worldwide / U-NII-2C / U-NII-2-Extended / U-NII-2e (5.470-5.725 GHz) at 5.6 GHz can be as high as +23 dBm (20 times higher than the maximum used 10 dBm) and if used the maximum distance can be multiplied accordingly. During the testing, it was observed that a metallic screen, placed between the transmitter and the receiver antennas, effectively blocking the RF trigger signal, switched the diode from the “ON” to the “OFF” state practically instantly, demonstrating the elimination of the RF triggered dynamically reconfigurable frequency notch of the proposed UWB antenna in real time.

6. Conclusion

A UWB monopole antenna with a dynamically reconfigurable notch-band at 5.6 GHz has been implemented and presented. This includes an associated rectenna with a customized DC-to-DC power booster that enables the dynamic creation of the notch in the presence of an external RF signal equal or stronger than -11 dBm. The UWB monopole has a rectangular slot, which hosts a J-shaped stub, and is connected to the radiator through a single PIN diode. With the diode in the "OFF" state the antenna operates as a conventional UWB radiator. When the diode is set to the “ON” state the J-shaped stub is electrically connected to the radiator, which causes the creation of a notch-band with a bandstop filter effect. The notch creation causes a significant reduction in the gain and improves the received SNR by rejecting the relatively strong interfering signal at 5.6 GHz. The interfering signal is detected and received from a rectenna that consists of a high gain (7.6 dB) patch antenna and a voltage doubler. The rectified 5.6 GHz input signal from the rectenna is used as an enabling voltage to a cascaded active DC-to-DC power booster with an output terminal connected to the biasing pads of the UWB antenna’s PIN diode. Using its $V_{CC}$ biasing voltage, the booster provides the PIN diode with sufficient...
power to switch to the “ON” state. As a result, the detection of a -11 dBm, 5.6 GHz signal leads to the effective cancellation of the interferer. When the received interferer signal is eliminated, the frequency notch immediately disappears, practically in real time, and the UWB antenna returns to a conventional UWB radiator mode. The switching can be repeated as many times as the 5.6 GHz interferer appears or disappears, verifying a dynamic response and the dynamic reconfigurability of the proposed UWB antenna, triggered only from external interfering or controlling signals.

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8. References
