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Dynamically Reconfigurable UWB Antenna using a FET Switch Powered by Wireless RF Harvested Energy

Abdul Quddious, Member, IEEE, Muhammad Ali Babar Abbasi, Member, IEEE, Marco A. Antoniades, Senior Member, IEEE, Photos Vryonides, Member, IEEE, Vincent Fusco, Fellow, IEEE, and Symeon Nikolaou, Member, IEEE

Abstract—A dynamically reconfigurable dual-layer UWB antenna integrated with an energy harvesting system for powering a GaAs FET switch is presented. The UWB antenna dynamically creates a notch-band in the presence of an interfering signal at 5.6 GHz and it goes back to normal UWB operation when the interferer is removed. For the switching operation, the FET switch is powered using only harvested energy carried by the interfering signals. The UWB antenna on the front layer is a microstrip-fed monopole with an embedded elliptical slot. Inside the slot a quarter wavelength linear stub acts as a resonator and it is connected and disconnected using the low-power FET switch. The UWB antenna shares the RF ground with a very compact energy harvesting system, that consists of a planar inverted-F antenna, a very compact voltage doubler rectifier and a passive DC-to-DC boost converter. The boost converter elevates the rectified voltage to above the 3.3 V threshold, which is the minimum voltage needed for the actuation of the FET switch. The dynamic notch-band reconfiguration of the UWB antenna without the need for any external DC power source is made possible when the collected power at the input of the rectifier is higher than -12 dBm.

Index Terms—Notch-Band UWB antenna, Reconfigurable, Rectifier, Wireless power transmission (WPT)

I. INTRODUCTION

In the past decade, Ultra-wideband (UWB) technology has received great attention due to the supported high data rates, the low transmitted power (-44.3 dBm) requirements, and its inherent resistance to jamming and relative immunity to multipath fading [1, 2]. Due to these advantages, it has been used in a wide range of different applications such as cognitive radio [3], short-range in-house communications, wireless sensor networks, healthcare and biomedical wireless systems [4], sensing and imaging systems [5], radar detecting and target locating [6] and IoT applications [7, 8].

Unavoidably, UWB systems share the same spectrum with several other narrowband wireless systems which use sub-bands within the Federal Communication Commission (FCC) defined UWB range which covers the spectrum of 3.1–10.6 GHz [9]. The FCC mask limits the UWB EIRP to -41.3 dBm/MHz, which means that wirelessly transmitted UWB signals are rather weak, and therefore they do not substantially affect the performance of the co-existing communication systems. The most popular and most congested band is the 5 GHz IEEE 802.11a/h/j/n WLAN systems using several narrow sub-bands between 5 and 6 GHz (5.15–5.35 GHz, 5.25–5.35 GHz, 5.47–5.725 GHz, 5.725–5.825 GHz). In order to suppress the unintentional received interfering signals and improve the associated SNR, UWB antennas are designed with notch-bands, which effectively filter out the received signals. UWB antennas with single-, dual- and multi band notches have been reported [4, 10, 11]. A number of methods are used to achieve a band-notched UWB antenna like to embed slots, add resonators on the radiator, or the feeding line, or even create perturbations on the RF ground [4, 10, 11].

In order to make the notch-band UWB antennas reconfigurable, electrical switches are used to connect and disconnect antenna parts. Some designs have used PIN diodes [3, 12] to switch the notch ON/OFF, varactor diodes [13] for continuously tunable designs, radio frequency micro-electromechanical systems (RF-MEMS) [14, 15] and GaAs field effect transistors (FET) [16] to achieve reconfigurability. In general, all these electrical switches need direct-current (DC) power to bias the switch components which means that a battery is required with the antenna module. Depending on the preferred switch the biasing conditions may vary significantly. The biasing voltage for the RF-MEMS (30–90 V) and varactors (0–33 V) is considerably higher as compared to PIN diodes (0.3-0.9V), however PIN diodes require a higher biasing current (mA range) and the overall DC biasing power required is in the range of mW.
The DC voltage, and when the DC voltage reaches 3.3 V it drives a passive DC-to-DC boost converter to elevate the DC voltage, and when the DC voltage reaches 3.3 V it actuates a FET switch on the UWB radiator, thus dynamically creating a notch-band at the frequency of the interferer. The development of a very compact and highly efficient RF-to-DC rectifier as part of a complete energy harvesting circuit was necessary.

For the proposed dual-layer dynamically reconfigurable UWB antenna presented in this work, a dual-layer concept was adopted and is presented schematically in Figs 1(b) and 1(c). The dual-layer module consists of a reconfigurable UWB antenna using a single low-power FET switch on one layer and an integrated very compact energy harvesting system consisting of a PIFA antenna with a high efficiency RF-to-DC rectifier (operating at narrow band 5.6 GHz WLAN frequency), cascaded with a passive DC-to-DC boost converter on the other layer. To the best of our knowledge, such a reconfigurable UWB antenna with dynamic battery-less reconfigurability, using switch powered entirely from RF harvested energy, is presented for the first time.

### Table 1: Schematic Dimensions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>W</td>
<td>27.0</td>
</tr>
<tr>
<td>L1</td>
<td>33.0</td>
</tr>
<tr>
<td>L2</td>
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<tr>
<td>l2</td>
<td>13.6</td>
</tr>
<tr>
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<td>2.5</td>
</tr>
<tr>
<td>l4</td>
<td>2.4</td>
</tr>
<tr>
<td>l5</td>
<td>6.0</td>
</tr>
<tr>
<td>r1</td>
<td>7.5</td>
</tr>
<tr>
<td>r2</td>
<td>27.3</td>
</tr>
<tr>
<td>r3</td>
<td>28.6</td>
</tr>
</tbody>
</table>

Fig 1. (a). Block diagram of the dynamically reconfigurable UWB antenna’s operation. Proposed UWB monopole antenna diagram: (b) and (c) System-on-Package (SoP) perspective view (d) front view, (e) rear view (d) Rectenna module.

Investigating the implementation of a battery-less switching solution, to be powered using harvested RF energy, a FET switch was considered since it requires a biasing DC power in the order of μW [17]. Furthermore, GaAs FET switches have a very low insertion loss compared to PIN diodes. The current manuscript presents a UWB antenna with an integrated energy harvesting (EH) circuit that allows the dynamic switching of the notch-band without the need for an external DC power source. The notch is created dynamically when an interfering signal is detected and it disappears when the interfering signals become undetectable. This is accomplished by exploiting the RF energy carried by the interfering signals, which is converted to DC using a high efficiency RF-to-DC rectifier. The rectified DC voltage drives a passive DC-to-DC boost converter to elevate the DC voltage, and when the DC voltage reaches 3.3 V it renormalizes the SMA connector was included in the simulation for better agreement between the simulated and measured results the SMA connector was included in the simulation model. An elliptical-slot on the radiating patch is introduced, this causes the band-pass filter to become band-reject, which affects the matching [13]. The matching is also improved with the addition of a rectangular cut with a size of 0.0009 mm²) with overall board dimensions 33 (L) × 27 (W) × 0.787 (h) mm³.

A 2.4 mm wide, microstrip line resulting in 50 Ω characteristic impedance is used to feed the UWB antenna. For matching improvement, the radiating patch and the RF ground patch have rounded corners. This contributes towards a smoother transition from the propagating modes to the radiated modes which affects the matching [13]. The matching is also improved with the addition of a rectangular cut with a size of $l_2 \times w_2$ at the top edge of the curved ground placed under the microstrip line on the back side of the substrate (see Fig. 1(d)). For better agreement between the simulated and measured results the SMA connector was included in the simulation model.

II. RECONFIGURABLE UWB ANTENNA DESIGN

The geometry of the UWB monopole antenna with the embedded elliptical slot is illustrated in Fig. 1. The UWB antenna was fabricated on a Rogers RT/duroid 5880 substrate (relative permittivity $\varepsilon_r = 2.2$, and loss tangent tan δ = 0.0009) with overall board dimensions 33 (L) × 27 (W) × 0.787 (h) mm³.

A 2.4 mm wide, microstrip line resulting in 50 Ω characteristic impedance is used to feed the UWB antenna. For matching improvement, the radiating patch and the RF ground patch have rounded corners. This contributes towards a smoother transition from the propagating modes to the radiated modes which affects the matching [13]. The matching is also improved with the addition of a rectangular cut with a size of $l_2 \times w_2$ at the top edge of the curved ground placed under the microstrip line on the back side of the substrate (see Fig. 1(d)). For better agreement between the simulated and measured results the SMA connector was included in the simulation model. An elliptical-slot on the radiating patch is introduced, this has a dual function. On the one hand it further improves the matching as can be seen in Fig. 2. On the other hand, without increasing the overall antenna size, it provides sufficient space for the integration of the linear $\lambda/4$ stub, which causes the band-
notch response. The effect of the rounded corners and the addition of an elliptical slot on the matching can also be seen in Fig. 2. Compared to the initial model that used rectangular patches for both the radiator and the ground (green dotted line) the combination of the elliptical slot on the radiator and the rounded edge ground (black line) improves the impedance matching significantly.

The elliptical slot provides the required space for the addition of a λ/4 stub (at 5-6 GHz) which causes a bandstop filter response in the same frequency range which is congested by several applications. The bandstop filter response can be added on the conventional UWB antenna response automatically and immediately once an interfering signal stronger than -12 dBm is received. For the generation of notch, the use of an external DC power source or any other control signal are not required.

Figs 1(c) and 1(d) present the proposed antenna schematic with the used dimensions which are summarized in Table I. For the electronic switching, a single pole, double-throw (SPDT) GaAs FET switch from Skyworks [18] is used (SKY13298-360LF). This switch can be driven directly by a 3.3 V DC voltage without the need for a bias network or a blocking capacitor, and it is suitable for frequencies from 3 to 8 GHz. For the biasing of the FET switch, vias are used which connect the front side where the FET is incorporated with small square pads of 1 × 1 mm² on the back side of the antenna where the energy harvesting circuit resides. The use of vias replaces the use of DC wires which may perturb the radiation pattern of the antenna. The total DC power consumption of the GaAs FET switch is very low (<16 µW). The required DC voltage is provided from DC-to-DC boost converter that is introduced in a subsequent section. CST Microwave Studio was used for the full wave simulations and complementary co-simulations using S-parameters files (.s3p) for the FET switch in both “ON” and “OFF” states, were carried out in Design Studio.

III. BAND REJECTION

The simulated surface current distribution at 5.6 GHz, the central frequency of the notch-band, can be seen in Fig. 3 when the switch is in “ON” or “OFF” state. With the switch in “ON” state the current distribution concentrates on the stub and as a result, a notch is created on |S₁₁| along with a significant decrease in the measured gain as can be verified from the subsequently presented in Fig. 9.

The geometric characteristics of the stub (length, width and position) directly affect the frequency and the quality of the notch band. The position and length of the stub are chosen in such a way to directly connect it to the 50 Ω feedline and thus resemble a standalone λ/4 planar monopole resonator. The resonance frequency of a λ/4 planar monopole can be approximated by [19]:

\[
f_r = \frac{c \times 0.24}{(h_{eq} + r_{eq}) \times \sqrt{\varepsilon_{eff}}}
\]

\[
h_{eq} = L_{monopole} = L_{stub}
\]

\[
r_{eq} = \frac{W_{monopole}}{2\pi}
\]

Where, \(c\) is the speed of light, \(h_{eq}\) and \(r_{eq}\) are the equivalent height and radius. The calculated resonance frequency 5.6 GHz using equation (1), (2) and (3) with \(h_{eq} = 9.75\ mm\), \(r_{eq} = 5.6\ mm\) and \(\varepsilon_{eff} = 3\) as shown in Fig. 5.
GHz, 5.25–5.35 GHz, 5.47–5.725 GHz and 5.725–5.825 GHz). The 5.0 GHz IEEE 802.11y HIPERLAN/2 band (5.470–5.725 GHz) demonstrates a practical implementation of the presented antenna. The fine-tuned optimized stub are placed on the elliptical slot of the UWB antenna, and by electrically connecting it to the feedline using the FET switch causes a notch at 5.6 GHz. It should be noted that the calculated one (indicated by the vertical grey dashed line) has the same resonance frequency as the peak of the notch-band and the calculated one (indicated by the vertical grey dashed line) has the same resonance frequency as the peak of the notch-band. From Fig. 4, it can be seen that the center frequency and the peak of the notch-band are controlled by the physical dimensions of the stub. The stub parameters can be modified to demonstrate the practical implementation of the presented concept when it is required to reject signals from the 5 GHz IEEE 802.11y HIPERLAN/2 band (5.470–5.725 GHz) and the 5 GHz IEEE 802.11y HIPERLAN/2 band (5.47–5.725 GHz). From Fig. 5, it can be seen that the center frequency in “ON” state is received at 5.6 GHz and 5.6 GHz. Fig. 4 shows that the simulated gain with simulated radiation patterns in the E- (x-z plane) and H- (y-z) planes at 5.6 GHz with the FET switch in the “ON” and “OFF” states. A direct comparison between the simulated power FET switch is set to the “OFF” state a typical UWB antenna creates a frequency notch which happens in an automatic and dynamic way when an interfering signal is present in both cases when the FET switch is “OFF” and “ON”. The 56 GHz x-z plane switch OFF 5.6 GHz x-z plane switch ON and 5.6 GHz y-z plane switch OFF 5.6 GHz y-z plane switch ON for the prototype and radiation pattern measurements were taken in “ON” state are presented in Fig. 7. The plots measured for 4 GHz x-z plane 4 GHz y-z plane 8 GHz x-z plane 8 GHz y-z plane with the FET switch in the “ON” and “OFF” states. This effectively creates a notch band which can suppress signals from 5.6–6.0 GHz. For field radiation pattern measurements with the FET switch in “ON” state are presented in Fig. 7. The plots measured for 4 GHz x-z plane 4 GHz y-z plane 8 GHz x-z plane 8 GHz y-z plane with the FET switch in the “ON” and “OFF” states. This effectively creates a notch band which can suppress signals from 5.6–6.0 GHz. The reflection coefficient for the prototype device. A direct comparison between the simulated reflection coefficient is presented in Fig. 6. When the low-

IV. UWB ANTENNA MEASUREMENT RESULTS

The effectiveness of the proposed UWB antenna as function of frequency for both cases when the FET switch is “OFF” and “ON” is presented in Fig. 9. The plots measured for 4 GHz x-z plane 4 GHz y-z plane with the FET switch in the “ON” and “OFF” states.
and 8 GHz indicate that the resonating element does not significantly affect the antenna radiation performance at frequencies other than 5.6 GHz. On the other hand, setting the FET switch to the “ON” state has a direct effect on the maximum gain of the antenna at 5.6 GHz. Fig. 8, presents the far-field radiation pattern at 5.6 GHz for both the “ON” and “OFF” states. H-plane (y-z plane) patterns are mostly omni-directional, while the E-plane (x-z plane) patterns have the typical shape for monopole-like radiators for every frequency. Note that the simulated patterns presented in Figs 7 and 8 follow the same Cartesian coordinate system depicted in Fig. 1. The measurements were conducted in NSI anechoic chamber using the same relevant coordinate system. The radiation pattern gain degradation observed from the normalized patterns in Fig. 8 is verified in Fig. 9, which presents the antenna peak realized gain values and efficiency versus frequency. The effective gain decrease at the notch frequency (5.6 GHz) is verified for both simulated and measured gain values, which present remarkable agreement.

V. ENERGY HARVESTING (EH) SYSTEM

In order to dynamically bias the FET switch, an Energy Harvesting (EH) system was implemented and integrated on the back side of the reconfigurable UWB antenna. Although the required power for the actuation of the GaAs FET switch is very low (<16μW) it still needs a minimum of 3.3V, current consumption is estimated to be 5 μA. This DC voltage level cannot be achieved by the rectifier, therefore a second DC-to-DC converter is needed. The implemented energy harvesting circuit consists of a PIFA antenna, a rectifier and a passive DC-to-DC boost converter. The antenna and the rectifier operate at 5.6 GHz and the implemented rectenna receives the RF incident signal and converts it into a DC voltage, which is subsequently further raised by the cascaded DC-to-DC boost converter. The FET switch of the UWB antenna is biased directly from the output of the DC-to-DC boost converter and this way when the FET is set to the “ON” state, it dynamically creates the notch, exploiting the harvested RF power of the received interfering signal. The 5.6 GHz RF signals which are part of the desired UWB signal are very low in power and they remain undetected from the rectenna. When the UWB antenna operates as a transmitter, an enabling signal is required to cancel the biasing of the FET switch and prevent the creation of the notch. The layout diagram of the rectenna with the cascaded DC-to-DC boost converter can be seen in Fig. 1(e), while the fabricated prototype mounted on the back side of the reconfigurable UWB antenna can be seen in Fig. 18 (b).

A. Rectenna

To accommodate the dual-layer microstrip technology with the rather limited available space on the second layer (the back side of the UWB antenna), without affecting its radiation performance, a very compact antenna for the energy harvesting system had to be selected. Considering the requirements for a very compact design and omni-directional radiation performance, a standard meandered planar inverted-F antenna (PIFA) [20, 21] was designed (Fig. 10). In addition to its compact size, the antenna had to be wideband enough to cover the entire frequency range from 5 to 6 GHz. The designed PIFA consisting of a meandered strip of width \( w_{\text{sh}} = 0.3 \, \text{mm} \), was the result of a rigorous multi-goal optimization process performed in CST Microwave Studio that resulted in the physical dimensions which are summarized in Table I. The PIFA is extended beyond the edge of the common RF ground with the UWB radiator. The same Rogers RT/duriod 5880 substrate was used for the PIFA as the one used for the UWB antenna. The primary design goal was to achieve a high radiation efficiency using a very compact (2.5 ×12.5 mm²) footprint area. The feeding point of the antenna was selected keeping in mind that it needs to be part of a rectenna system and a DC-to-DC booster to implement a compact complete energy harvesting system. For testing purposes a stand-alone PIFA was fabricated and measured. The implemented PIFA with identical RF ground with the UWB antenna can be seen in the inset of Fig. 10 with the S-parameter measurements of the antenna. The \( |S_{11}| < -10 \) dB bandwidth of the antenna, covers the range of 5-6.1 GHz, overlapping with the frequency bands used for the most common interfering applications. Fig. 11 shows the simulated and measured normalized radiation patterns of the compact PIFA in the E- (x-z) and H- (y-z) planes at 5.6 GHz.
so a directive antenna having larger footprint and the same efficiency can enhance the value of $P_{\text{out}}$.

Considering the design constraints, the primary objective was a very compact circuit footprint that could use the rounded UWB antenna ground patch and provide a high RF-to-DC efficiency for varying input power levels (-25 dBm to 0 dBm). The rectifier was designed as part of the complete energy harvesting circuit and as the intermediate component between the PIFA and the passive DC-to-DC boost converter. The desired increased efficiency for the lower end of the incident power range led to the selection of a voltage-doubler topology with Schottky diodes. The low-cost Schottky diode SMS7630-079LF [22] from Skyworks was selected, and the Keysight – Advanced Design System (ADS) software was used for harmonic-balance (HB) and large signal S-parameter (LSSP) simulations to analyze the non-linear behaviour of the rectifier circuit. The rectifier bandwidth is defined by the matching network, therefore a distributed element matching network was preferred for enhanced bandwidth. The rectifier’s efficiency was optimized considering the termination load to be 3.9 KΩ which matches the input impedance of the subsequent DC-to-DC boost converter. The design parameters are summarized in Table I and Fig. 12 presents the reflection coefficient and the fabricated rectifier prototype. Efficiency is defined in (4) where, $P_{\text{out}}$ and $P_{\text{in}}$ are the DC output power and the RF input levels, respectively, and $V_{\text{dc}}$ is the rectified voltage across the terminals of the load, $R_L$.

$$\eta = \frac{P_{\text{out}}}{P_{\text{in}}^{\text{out}}} = \frac{V_{\text{dc}}^2}{P_{\text{in}}} \times 100\% \quad (4)$$

The voltage doubler topology includes two capacitors. The optimized values for $C_{\text{c1}}$ and $C_{\text{c2}}$ were 82 pF and 120 pF, respectively. For input power at -12 dBm the measured return loss is more than 10 dB from 5.4 to 5.8 GHz (measured bandwidth of 400 MHz). For efficiency measurements, the R&S® SMF100A signal generator was used to generate the signals that fed the rectifier. The rectified DC voltage across the $R_L$ was recorded while the power of a single tone 5.6 GHz signal was varied from -27dBm to 0 dBm. Fig. 13 presents the measured rectified voltage and the corresponding efficiency in comparison with the simulated results. Equation (4) was used for the efficiency calculation. Measurements indicate that the efficiency varies from ~30% at -15 dBm up to ~46% at -5 dBm. The performance and the size of the implemented rectifier is compared with previously reported rectifiers in Table II. The variations of the efficiency and the output voltage over a wide frequency range can be seen in Fig. 14. It can be observed that the used rectifier has generally a good efficiency at low input powers (40% at -10 dBm), and at the same time it is the most compact design, in comparison with other designs which are listed in Table II. The next smallest rectifier [24] has an area that is 6 times larger than the circuit footprint of the proposed rectifier. As can be verified from Fig. 15 the efficiency of the rectifier depends non-linearly on the termination load which in this case is the input impedance of the subsequent DC-to-DC boost converter. For the simulated efficiency presented in Fig. 15, $R_L$ was varied from 100 Ω to 1 MΩ while varying input power levels were used. The rectifier’s efficiency was
oscillation’s start-up conditions. When the initial stage of the
and the low drain current ($P_{DD}$) of the JFET satisfies the
simultaneously to form an Armstrong oscillator, working as a
gate-source PN junction of the JFET, used as a diode to rectify
inductors $L_1$ and $L_2$ of the transformer. The third block is the
gate-source PN junction of the JFET, used as a diode to rectify
output capacitor is enforced on the JFET’s gate, eventually
saturation of the primary current, the voltage across the primary
capacitor ($C_2$) starts to charge with a negative voltage. Upon
saturation of the primary current, the voltage across the primary
cap is led to zero. Simultaneously, the negative voltage of the
output capacitor is enforced on the JFET’s gate, eventually
pinching it off. As a result, the primary coil current is decreased
and an opposite sign voltage is applied by the secondary coil on
the gate of the JFET, which sets the switch to the “OFF” state.
When the maximum voltage that switched-off the JFET returns
to zero the cycle of oscillation is repeated.

When the maximum voltage that switched-off the JFET returns
to zero the cycle of oscillation is repeated.

B. Passive DC-to-DC Boost Converter

In order to actuate the SKY13298-360LFA GaAs FET
switch, a control DC voltage of 3.3 V in combination with a 5
µA current, are needed. In this section, an ultra-low power
passive DC-to-DC boost converter is presented that was needed
in order to boost the rectifier’s output DC voltage from a few
milli volts to a few volts. The Armstrong oscillator [29] inspired
the design of the converter. The passive DC-to-DC boost
converter used in this work is an ultra-low-voltage, self-
powered converter. The converter schematic is shown in Fig.
16. At the input stage an AND gate can be added which can use
the Vin and an enabling signal from the DSP as inputs. The DSP
creates a logic zero when the UWB transceiver is in
transmission mode and thus the DC-to-DC converter will not
raise the Vin voltage to the level which sets the FET switch
on. As a result, the notch will not appear when the UWB
antenna is used as a transmitter. For the implemented circuit in
Fig. 19(b) the AND gate was not included since the UWB
antenna was tested as a receiver. The used converter has three
major functional blocks. The first block is a voltage stepping up
Colpitts “LPR6235-752SMRB” transformer. The second
block consists of a J201 JFET which also uses the coupling
inductors $L_1$ and $L_2$ of the transformer. The third block is the
gate-source PN junction of the JFET, used as a diode to rectify
the JFET’s gate oscillations. These three functional blocks work
simultaneously to form an Armstrong oscillator, working as a
DC-to-DC converter. The low Gate-Source cut-off voltage ($V_P$)
and the low drain current ($I_{DD}$) of the JFET satisfy the
oscillation’s start-up conditions. When the initial stage of the
transformer is connected to the output terminal of the rectifier,
the current rises on the primary coil $L_1$ and it induces a voltage
on the secondary winding, applying a positive voltage on the
normally-on N channel gate of the JFET. The PN junction
between gate and source is conducting, therefore the output
capacitor ($C_2$) starts to charge with a negative voltage. Upon
saturation of the primary current, the voltage across the primary
cap is led to zero. Simultaneously, the negative voltage of the
output capacitor is enforced on the JFET’s gate, eventually
pinching it off. As a result, the primary coil current is decreased
and an opposite sign voltage is applied by the secondary coil on
the gate of the JFET, which sets the switch to the “OFF” state.
When the maximum voltage that switched-off the JFET returns
to zero the cycle of oscillation is repeated.

Fig. 17 shows the measured DC output voltage across the output
capacitor ($C_2$) at the output stage of the passive DC-to-DC
converter in comparison with the input DC voltage from the
output of the rectifier. The rectifier with input RF power ($P_{in}$)
equal to ~12 dBm (set at the signal generator during testing)
results in a rectified voltage at the output of the rectifier equal
to 0.31 V, which in turn results in a boosted voltage of 3.3 V at
the output of the DC-to-DC converter, as can be seen in the
highlighted region of Fig. 17. This boosted voltage is applied
through the vias directly on the FET and causes its actuation.
Although the available power at the FET switch is smaller than
the power at the output of the rectifier, the omission of the
passive DC-to-DC boost converter is not feasible. It is needed
in order to deliver the combination of the high DC voltage with
the low current (μA range) that eventually causes the effective switching of the FET.

VI. IMPLEMENTATION AND TESTING

A. Implementation

Fig. 18 shows the fabricated prototype of the battery-less UWB antenna with the dynamically reconfigurable notch powered from the EH system. On the front side (Fig. 18(a)), the UWB monopole with the FET switch can be seen, while the rear view (Fig. 18(b)) shows the integrated energy harvesting circuitry in a System-on-Package (SoP) approach. Both the UWB antenna and the energy harvesting system were fabricated with a milling machine (LPKF ProtoMat H100) on two distinct Rogers RT/duroid 5880 boards. The GaAs FET switch (Skyworks SKY13298-360LF [18]) was soldered on the first board with the help of the LPKF ProtoPlace on the designated IC land patterns shown in Fig. 1 (c), while the SMS7630 [22] Schottky diodes and the capacitors (Murata series GJM03-82/120 pF [30]) were added on the second board for the implementation of the rectifier component. Finally, a voltage step up transformer (Coilcraft LPR6235-752SMRB [31]) with a J201 JFET and 1 nF capacitor were mounted on the EH system board for the passive DC-to-DC boost converter implementation. The two boards were placed back-to-back and were stuck together with low-loss glue so that the rounded RF ground patches of the two boards coincided with each other.

B. Testing

For the testing of the SoP reconfigurable UWB antenna the setup of Fig. 19 was used. In Step 1 (Fig. 19(a)), the received RF power ($P_{\text{Rx}}$) from the PIFA was measured using a spectrum analyzer (Anritsu MS2668C) to define the received input RF power to the rectifier, at a given distance $d$. A microstrip rectangular patch antenna [32] with realized gain 7.6 dBi and radiation efficiency 97% was connected to the signal generator (R&S SMF100A) and was used as the RF power transmitter. In Step 2 (Fig. 19(b)), the rectified DC voltage at the same distance $d$ was measured at the output of the rectenna and the output terminals of the passive DC-to-DC booster. When the rectified DC voltage rose above $V_{\text{rec}} = 0.31$ V, the low power passive DC-to-DC boost converter elevated the output voltage to 3.3 V and successfully actuated the FET switch that is a constituent part of the reconfigurable UWB antenna. When the rectified voltage was lower than 0.3 V, the boosted output voltage of the passive DC-to-DC booster dropped below 3 V, the FET switch returned to the “OFF” state and the notch disappeared, in response. For rectified DC voltage higher than 0.31 V at the output of the RF-to-DC rectifier, an input RF power ($P_{\text{in}}$) higher than -12 dBm is required. This $P_{\text{in}}$ can be delivered to the rectifier by any antenna type.

In order to experimentally verify the expected response of the proposed dynamically reconfigurable UWB antenna (Fig. 18(a)), S-parameter measurements were taken (setup of Fig. 19(b)) in both the presence and the absence of an interfering 5.6 GHz RF signal. Using the signal generator RF signals of varying power levels were created and transmitted using the patch antenna. The generated, unmodulated, single tone 5.6 GHz signal, was received and rectified from the EH system of the dynamically reconfigurable UWB antenna. The transmitter and receiver antennas, remained in each other’s far field, during the entire set of measurements. The collected RF signal was rectified, boosted and was used to bias the packaged FET in order to dynamically actuate the notch on the $S_{11}$ response of the UWB antenna. The testing was repeated for three different power levels while the distance was varied, and the measurement results are summarized in Fig. 20. Regarding the labeling used in Fig. 20, $P_{\text{out}}$ is the generated power from the signal generator, and $P_{\text{Rx}}$ is the received power at the input of the rectifier. The actuation of the packaged FET switch (requires at least 3.3 V) was made possible when $P_{\text{Rx}}$ was higher than -12 dBm.

The three power levels ($P_{\text{out}}$) for which measurements were taken at 5, 10 and 15 dBm. The dynamic reconfiguration of the UWB antenna was observed at distances ($d$) 0.15, 0.25, and 0.4 m and this can be derived from Fig. 20. The maximum transmitted power of Unlicensed National Information Infrastructure (U-NII) at 5.6 GHz as defined by FCC is limited to +23 dBm which means that the maximum distance for successful actuation could be increased accordingly. To verify
the response speed of the dynamically reconfigurable notch a metallic screen was used to obstruct the transmitted signal. As a result, the FET switch was turned from “ON” state to “OFF” state practically instantly. The biasing of the FET switch, and therefore the reconfigurability was powered only from the wireless RF harvested energy, without using any external DC power source.

C. Applications

The UWB is generally divided into 5 band groups, and each band groups have 2 or 4 sub-bands. In standard UWB communication protocol, we see the usage of an entire band group and sub-bands are allocated for time sequencing signal transmission/reception [33]. One application of our approach is to use 1 sub-band in a band group for energy harvesting, and the remaining sub-bands for low data rate communication application like in battery-less autonomous wireless communication nodes. Practical applications of EH system integrated with UWB radiators can be found in the implementation of battery-less UWB RFIDs used for indoor localization. Moreover, as an improvement in UWB antennas with permanent stop bands, this work shows that a battery-less system can be connected to a UWB antenna to turn the stop band operation ON or OFF dynamically and wirelessly.

VII. CONCLUSION

A dynamically reconfigurable UWB antenna integrated with an energy harvesting system in a dual-layer SoP approach has been presented. In order to enable the powering of the electric switch from the harvested wireless RF energy originating from interfering signals at 5.6 GHz, a very low power GaAs FET switch was chosen. The energy harvesting system is designed to collect power from interfering signals in the congested band around 5.6 GHz. It consists of a PIFA cascaded with a very compact RF-to-DC rectifier with a recorded efficiency of 40% at -10 dBm. The final stage is a passive DC-to-DC boost converter based on the Armstrong oscillator which elevates the rectified DC voltage to the required 3.3 V which is the minimum voltage needed for the actuation of the packaged FET switch. For the designed energy harvesting system, the minimum RF power that is needed at the input of the rectifier is -12 dBm. At collected power levels higher than -12 dBm, the FET switch of the reconfigurable UWB antenna is set to the “ON” state, causing a notch-band around 5.6 GHz. As soon as the 5.6 GHz interfering signal is eliminated, the notch-band is immediately cancelled and it appears again dynamically when the received power goes higher than the -12 dBm threshold, without the need of any external DC source, since the FET switch is powered only from the harvested energy carried by the collected interfering 5.6 GHz signals. In the absence of an interfering high power 5.6 GHz signal, the presence of the extended narrow band PIFA and the miniaturized EH system do not disturb the wideband standard operation of the UWB antenna in neither reception nor transmission mode.

The proposed UWB antenna is a microstrip-fed monopole with an embedded elliptical slot. A quarter wavelength linear stub is placed in the elliptical slot and a GaAs FET switch serves as the electric switch that connects and disconnects the stub causing the creation and the elimination of the notch-band, respectively. The UWB antenna is fabricated on a Duroid substrate. The narrow band EH system is implemented on a second Duroid substrate and it shares a common RF ground with the UWB antenna. The harvested energy has a direct relationship with the antenna gain and overall footprint. However, for the implementation of the proposed SoP reconfigurable UWB antenna, we consider a miniaturized EH system. Therefore, a compact PIFA with omnidirectional radiation patterns is used as the rectenna module cascaded with a very compact RF-to-DC rectifier using a voltage-doubling topology.

To the best of our knowledge a dynamically reconfigurable UWB antenna with switching operation powered entirely from RF harvested energy is presented for the first time. Considering the FCC EIRP constraints for both UWB and WLAN systems the proposed SoP UWB antenna can be an excellent candidate for Wireless Personal Area Network (WPAN) applications that require improved SNR, since it dynamically suppresses the interfering signals in the congested frequency band around 5.6 GHz. SoP implementations of compact EH systems with UWB antennas can also be used for UWB RFIDs used for indoor localization.

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