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Bidirectional DC-DC Resonant Converter Design for Electric Vehicle Charging Stations Integration to MVDC Grids

Ibrahim Alhurayyi
School of Electronics, Electrical Engineering and Computer Science
Queen’s University Belfast, United Kingdom
ialhurayyi01@qub.ac.uk

Ahmad Elkhateb
School of Electronics, Electrical Engineering and Computer Science
Queen’s University Belfast, United Kingdom
a.elkhateb@qub.ac.uk

D John Morrow
School of Electronics, Electrical Engineering and Computer Science
Queen’s University Belfast, United Kingdom
dj.morrow@qub.ac.uk

Abstract—The extensive use of electric vehicles (EVs) needs ultra-fast-charging stations with high charging power greater than 22 kW (Pcharging >22kW). Medium voltage direct current (MVDC) grids can provide the best solution for the high demand of EVs and fast-charging stations infrasstructures with the existing distribution grids. The resonant bidirectional dual active bridge (DAB) DC-DC converter can be a promising technology in integrating an electric vehicle charging station (EVCS) with MVDC grid. This work focuses on the converter design with voltage control and stability by applying two different control strategies mainly: PI control and sliding mode control (SMC). All simulations will be performed in order to present the feasibility of the proposed solution and compare the results of each control methods. This comparison, for the first time, in the medium voltage level can open new insights for using nonlinear control methods such as SMC to ensure stability and obtain better dynamic performance.

Keywords—MVDC; electric vehicles charging stations (EVCSs), DAB converter, PI, sliding mode control (SMC)

I. INTRODUCTION

With the widespread using of electric vehicles (EVs), charging time reduction is the main challenge. The level 3 of fast charging can reduce the charging time to less than 15 minutes with the power rating in the range of 50-350 kW [1]-[3]. In the near future, the power demand for feeding these EVs may exceed the capacity of existed grids since the charging power depends on the capacity of electric cars that tends to be 60 kWh. Thus, Ultra-fast charging stations with charging power greater than 22 kW (Pcharging >22kW) that can use MVDC grids are considered as an optimal solution to supply charging loads [4]-[5].

Dual active bridge (DAB) DC-DC converter is a promising topology for MVDC grids. It has been getting a considerable attention because of its features, such as simplicity, galvanic isolation, high efficiency, and soft switching capability [6]. However, the soft switching is limited in wide voltage applications. Also, the conventional DAB is suffered from the high losses of turning off and high reverse energy. A wide range of control techniques have been proposed in the literature [7]-[11]. Although some control methods have been proposed to ensure the soft switching, they are complicated, and the soft switching can be achieved among certain operation zones [6], [12], [13].

Bidirectional resonant converters become a preferable topology in terms of DC-DC applications. Resonant converters provide several advantages such as soft switching capability for all switches, high frequency operations, and high efficiency with low EMI and few components [14], [15].

Several DC-DC converters for exploiting the maximum power have been discussed in the literature [16]-[18]. DC-DC resonant converters that utilize resonant compensation networks without additional resonant tanks have been discussed in [19]. LLC resonant converter consists of 3 resonant elements which is able to achieve the zero-voltage switching (ZVS) for switches in the inverter side. The bidirectional power flow necessities to replace the diodes in the rectifier bridge with fully controlled semiconductor devices [14]. However, in the reverse mode, the voltage gain is different. A symmetric resonant tank in CLLLC has been proposed, but this structure can result in the volume increase and possibility of errors in the analysis and design [20]. In contrast to the CLLLC structure, a LLC topology with 4 resonant components can achieve the ZVS in the inverter side at full-load range and the zero-current switching (ZCS) in the rectified side with a certain range of load. In addition, the similar voltage gain of the CLLLC configuration can be accomplished with neglecting one resonant inductor [14].

Thus, this work is to employ the CLLC resonant DAB DC-DC converter to couple the EV fast charging station to MVDC grids. Section II presents the EV charging station and it is followed by the proposed CLC resonant DAB in Section III. The control methods for the resonant DAB converter and EV charging unit are covered in Section IV. The simulation results are discussed in Section V.

II. EV CHARGING DESIGN

Before designing the charging station, there are a few aspects should be considered as the following [4]:

- Estimating the power demand of charging slots.
- The rate of maximum charging power for each vehicle.
- Network parameters, such as the power levels and nominal voltage.
A. EV Charging Station Rating Power

The charging station consists of two charging units to allow charging two electric vehicles simultaneously. The station is designed when all vehicles are charged in the fast charging mode. Thus, each vehicle needs a charging power equal to [21],[22]:

\[
P_{\text{charging}} = V_{\text{max}} I_{\text{max}} = 400 \times 200 = 80kW \quad (1)
\]

The nominal apparent charging power is selected to be:

\[
A = 160 \text{ kVA}
\]

![Fig. 1. CLLC DC-DC dual active bridge (DAB) converter.](image)

B. EV Battery Charging Converter

The converter topology used here is the half-bridge DC-DC converter in order to operate in buck mode and boost mode. In the buck mode, the battery is charged while the battery discharges in the boost mode. The boundary between the continuous and discontinuous conduction mode is necessary to determine the inductance’s minimum value. The ripple current is calculated as follows [23]:

\[
\Delta I = \frac{1}{L} \int_0^{t_{\text{on}}} v_L \, dt = \frac{V_{\text{DC}} - V_{n,\text{min}}}{L} t_{\text{ON}} = \frac{V_{\text{DC}} - V_{n,\text{min}}}{V_{\text{DC}} f_s} V_{n,\text{min}} \quad (2)
\]

The minimum value of the inductance with 10% ripple of the maximum current becomes:

\[
L < \frac{V_{\text{DC}} - V_{n,\text{min}}}{0.1 I_{\text{max}}} \frac{V_{n,\text{min}}}{V_{\text{DC}} f_s} \Rightarrow L < 1.7 \text{ mH} \quad (3)
\]

In the other hand, the battery capacitance can be calculated based on the peak-peak voltage ripple as the following:

\[
\Delta V_{\text{bat}} = \frac{\Delta Q}{C_{\text{bat}}} = \frac{1}{2 C_{\text{bat}}} \Delta I \frac{T_s}{2} \quad (4)
\]

The capacitance in case of minimum battery voltage and maximum ripple current can be expressed as

\[
C_{\text{bat}} > \frac{1}{8 \Delta V_{\text{bat}}} \Rightarrow C_{\text{bat}} > 111\mu F \quad (5)
\]

III. MEDIUM VOLTAGE DC-DC BIDIRECTIONAL RESONANT CONVERTER DESIGN

Few points are necessary to be considered prior to design the resonant bidirectional converter [6], [14].

- The converter design should achieve the voltage gains of both directions of power flow.
- Choosing the high operating frequency is important for the sake of reducing the size of both magnetics and output capacitor.
- Converter design should ensure the zero-voltage switching (ZVS) for primary bridge switches and soft commutation for switches in the secondary side. That results in higher efficiency.
- Lower circulating reactive energy in the resonant network requires the small phase shift angle between the input square voltage and current.

Besides, the converter design depends on some factors. First, transformer turn’s ratio should be determined. Choosing the value of magnetizing inductance is critical to meet ZVS requirements. Finally, the proper design of the resonant tank inductance and capacitors should be selected to achieve the voltage gains of both directions.

A. Converter Configuration

Fig. 3 shows the configuration of resonant DAB converter with the bidirectional power transfer. It is composed of two full-bridge converters operating under a fixed switching-frequency. The bridges are linked with a resonant tank which includes Cr1, Cr2, and a transformer for galvanic isolation. In addition, it includes the leakage inductance Lr and magnetizing inductance Lm. In the charging/forward mode, the power is conveyed from the DC bus to the charging stations, while in the reverse mode, the power is transferred from the station to the DC bus [14], [15].

B. Modulation Scheme

Many control techniques for DAB converters are proposed. The most commonly used is the single-phase shift modulation which is applying a 50% duty ratio to all switches in both bridges. Adjusting the angle of phase shift can control the average transferred power [6].

Another modulation method is the dual-phase shift (DPS) that offers an additional degree of freedom. This method can help in reducing the reactive power and enhancing the overall efficiency [6], [24], [25]. In the DPS, all the converter legs have different angles from each other while the gate signals are defined with 50% duty cycle. The phase shift angle can be calculated to satisfy one of the following conditions:

\[
0 \leq D_1 \leq D_2 \leq 1
\]

\[
0 \leq D_2 \leq D_1 \leq 1
\]

where D1 is the inner-phase shift between the legs in each bridge and D2 is the outer-phase shift between the primary and
secondary bridges. The average transferred power can be calculated as [25]:

\[ P = \frac{nV_{in}V_o}{2f_L} [D_2(1 - D_2) - 0.5D_2^2], \]

\[ 0 \leq D_1 \leq D_2 \leq 1 \]  

(6)

\[ P = \frac{nV_{in}V_o}{2f_L} [D_2(1 - D_1 - 0.5D_2)], \]

\[ 0 \leq D_2 \leq D_1 \leq 1 \]  

(7)

To control the power flow, the sum of both phase shifts must satisfy the following condition:

\[ D_1 + D_2 \leq 1 \]  

(8)

The base value of transferred power can be calculated as:

\[ P = \frac{nV_{in}V_o}{2\pi f_L} \]  

(9)

C. Converter DC Gains

The DC voltage gains for forward mode (mode-1) and reverse mode (mode-2) can be expressed as follows [14]:

\[ M_{mode-1} = \left( \frac{R_{ac} + Z_{cr2}}{Z_{cr1} + Z_{ac} + (R_{ac} + Z_{cr2})/Z_{lm}} \right) \frac{R_{ac}}{R_{ac} + Z_{cr2}} \]  

(10)

\[ M_{mode-2} = \left( \frac{R_{ac} + Z_{cr1}}{Z_{cr1} + Z_{ac} + (R_{ac} + Z_{cr1})/Z_{lm}} \right) \frac{R_{ac}}{R_{ac} + Z_{ac} + Z_{cr2}} \]  

(11)

D. The Transformer Turn’s Ratio

The transformer turn’s ratio \( n \) can be calculated as the following expression:

\[ n = \frac{N_1}{N_2} = \frac{V_{in}}{V_o} \]  

(12)

E. Selection of Magnetizing inductance

ZVS for all primary side switches can be achieved when the current through these switches is negative during the turning ON. The current magnitude is controlled by the magnetizing inductance value and dead time duration. The dead-time of this converter can be calculated in similar expression used for LLC resonant converter as,

\[ L_m \leq \frac{t_{dead}}{8f_s C_{ess}} \]  

(13)

where \( t_{dead} \) represents the dead-time between the upper and lower side switches in the same leg, \( f_s \) is the switching frequency, and \( C_{ess} \) implements the equivalent output capacitance of inverting switches.

The low magnetizing inductance increase the magnetizing current which results in high conduction losses. In contrast, the large magnetizing inductance leads to low magnetizing current and that can limit the voltage gain. It is possible to design a large magnetizing inductance for long dead-time. However, this can lead to high primary side RMS current [14].

F. Design the Inductance Ratio

The inductance ratio \( L_m \) is another factor which the voltage depends on. The small values of \( L_m \) put the operating frequency in the narrow range. Thus, it should be as large as possible results in the wide-range operating frequency. It can be defined as [14],

\[ L_n = \frac{L_m}{L_r} \]  

(14)

G. Design the Resonant Elements

After determining the switching frequency, magnetizing inductance and inductance ratio, the resonant elements \( L_r, C_r \) can be calculated as,

\[ C_r = \frac{1}{2\pi f R_{ac}} \]  

(15)

\[ L_r = \frac{1}{(2\pi f)^2 C_r} \]  

(16)

H. Selecting the Capacitance Ratio and \( C_{r2} \)

Selecting a proper value of capacitance ratio \( g \) to attain similar curves of the DC gains for both operating modes. It must make the curves of DC gains almost similar in the shape and amplitude. Once \( g \) is determined, the second resonant capacitor \( C_{r2} \) can be identified by,

\[ g = \frac{C_{r2}}{C_r} \]  

(17)
It can be clearly seen that the highest gains for forward mode and reverse mode can be achieved when Ln equals to 2 and Q is about 0.3.

IV. CONTROL SYSTEM

A. DC-DC DAB Converter Control

The bidirectional DC-DC converters have nonlinear elements which results in non-linear dynamic equation. Thus, the nonlinear control methods would be an appropriate choice.

1) Proportional Integral (PI) Controller: This control scheme is very common. It is simple and easy to implement but it has some shortcomings such as maximum overshooting with high settling time and low robustness against uncertainties. Therefore, its response is low to fast changes. It is used to control the output voltage. The input to the PI is the error voltage resulted from the subtracting the desired voltage and the measured output voltage. The PI output with the sum of both proportional and integral of error signal represents the duty-cycle of a pulse width modulation (PWM) [26], [27]. The arrangement of PI controller is shown in fig. 4.

![Fig. 4. PI control scheme for dual-phase shift (DAB) converter.](image)

2) Sliding Mode Control (SMC): The SMC is one of nonlinear control strategies which is advantageous in terms of fast response during finite-time and robustness in case of parameter variations or perturbation. The bidirectional DC-DC converters have nonlinear elements which results in non-linear dynamic equation. The SMC is beneficial for variations and disturbances with low sensitivity. The oscillating behavior is inherent in the SMC and it is called chattering phenomena. In general, the SMC involves two control loops: the inner current and outer voltage control loops. Fig. 5 shows the SMC arrangement. In the SMC, the sliding line can decide switching state u that is corresponding the turning On and OFF of the switch. The sliding surface S can be represented as a linear set of state variables. The switching function can be expressed as [28],

\[ S = \alpha_1 x_1 + \alpha_2 x_2 + \ldots + \alpha_n x_n = \alpha^T x = 0 \quad (18) \]

where \( \alpha^T \) represents a vector of the sliding surface coefficients.

The equation above describes the sliding line that passes the phase plane via the origin. The origin is called the stable operating point where the voltage error equals to zero. It can take a role of acting as a boundary by splitting the phase plane into two regions. Each region is allocated for a switching state to drive or steer the phase trajectory to the sliding line. The system would be stable if the phase trajectory reaches and follows the sliding line in the direction of the origin.

The sliding mode control is combined with direct power control. Since the filter capacitor at the output side is constant, the relationship between the power and energy inside the capacitor can be interpret as,

\[ \frac{dv_c}{dt} = \frac{d}{d(0.5Cv_c^2)} = P - P_0 = P - v_o(t)i_o(t) \quad (19) \]

\[ \frac{dv_o}{dt} = P - \frac{i_o(t)}{C} \quad (20) \]

where P is calculated in (6).

The output voltage error and its integral term can be used in design the controllers to rearrange (20) as [28],

\[ P = \frac{i_o(t)}{C} + \frac{\alpha_1}{\alpha_2} (v_o^* - v_o(t)) \]

\[ + \frac{\alpha_3}{\alpha_2} \int (v_o^* - v_o(t)) dt \]

\[ \frac{\alpha_1}{\alpha_2} = \frac{10}{T_s} \]

\[ \frac{\alpha_3}{\alpha_2} = \frac{25}{\xi^2 T_s^2} \]

\[ K_{p1} = \beta L_{Lr} \left( \frac{\alpha_1}{\alpha_2} - \frac{1}{R_{ac} C} \right) \]

\[ K_{p2} = L_{cr} \frac{\alpha_3}{\alpha_2} \]

\[ v_c = \left[ -\beta L_{Lr} \left( \frac{\alpha_1}{\alpha_2} - \frac{1}{R_{ac} C} \right) i_c + L_{cr} \frac{\alpha_3}{\alpha_2} (V_{ref} - \beta v_o) \right] \]

\[ f_{BW} = f_s \times \frac{1}{20} \]

\[ \omega_n = 2\pi f_{BW} \]

\[ T_s = \frac{4}{\xi \omega_n} \]

\[ \beta = \frac{V_{ref}}{V_o} \quad (28) \]

![Fig. 5. Sliding mode control scheme for (DAB) converter.](image)
TABLE I. LITHIUM-ION BATTERY PARAMETERS

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nominal Voltage $V_n$</td>
<td>400 V</td>
</tr>
<tr>
<td>Battery capacity</td>
<td>40 Ah</td>
</tr>
<tr>
<td>Open circuit voltage $V_{oc, max}$</td>
<td>454.3 V</td>
</tr>
<tr>
<td>Internal resistance</td>
<td>0.1 Ω</td>
</tr>
<tr>
<td>Minimum cut-out voltage $V_{bat, min}$</td>
<td>300 V</td>
</tr>
</tbody>
</table>

where $v_o^∗$ is the output voltage reference and $f_{BW}$ is the bandwidth of closed-loop.

B. EV Charging Converter Control

The battery charging control uses two control methods: constant current (CC) and constant voltage (CV). The battery will be charged by applying the constant current control in the beginning and then switching to voltage control scheme in order to prevent the high currents injection. In the constant current strategy, the battery operates as a current source while it mimics as a voltage source in the constant voltage method. The attained duty ratio defines which operation mode where the boost mode is defined by the CC scheme and the buck mode is realized by applying the CV method [22], [23].

V. COMPARISON AND DISCUSSION

Simulations are carried out for CLLC DAB using the PI and SM control methods. The system is composed of 4 CLLC DAB converters connected in the input parallel output parallel (IPOP) to feed two charging units for two electric vehicle. During the simulation, the two EVS start charging at $t=0.1$ s and then discharging at $t=0.3$ s until $t=0.352$ s to turn into charging mode. The two control methods have been investigated to offer maximum effort for ensure stability.

The obtained waveforms presented below are discussed. It can be clear that the settling time of SMC is shorter compared with the PI control. Therefore, a better dynamic response can be attained with SMC.

TABLE II. DC-DC RESONANT DAB CONVERTER PARAMETERS

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{in}$</td>
<td>3000 V</td>
</tr>
<tr>
<td>$V_{out}$</td>
<td>1500 V</td>
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<tr>
<td>$n$</td>
<td>2</td>
</tr>
<tr>
<td>$R_m$</td>
<td>73</td>
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<tr>
<td>$C_1$</td>
<td>129.77 nF</td>
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<tr>
<td>$L_1$</td>
<td>62.3 µH</td>
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<tr>
<td>$L_{m1}$</td>
<td>124.6 µH</td>
</tr>
<tr>
<td>$C_2$</td>
<td>129.77 µF</td>
</tr>
<tr>
<td>$f_s$</td>
<td>56 kHz</td>
</tr>
<tr>
<td>$C_o$</td>
<td>1250 µF</td>
</tr>
</tbody>
</table>

A. PI Controller

The output voltage and current of the DAB with PI controller are presented in fig. 6 where VDC ranges from 1466 V to 1472 V with no load and from 1452 V to 1460 V when charging the EVs. The settling time is 0.009 s. That is followed by the primary and secondary voltage waveforms of the transformer under dual-phase shift modulation in fig. 7. Also, the voltage, current, and state of charge (SoC) of EVs batteries are presented in fig. 8.

Fig. 6. DC link (a) voltage and (b) current with PI controller.

Fig. 7. Waveforms of primary and secondary voltages.

Fig. 8. Voltage, current, and SoC of the batteries of (a) EV1 and (b) EV2.

B. Sliding Mode Control

The sliding coefficients are calculated as $K_{p1}=0.1094$ and $K_{p2}=0.019549$. The $V_{ref}$ is chosen to be 500 and therefore $\beta$ equals to 1/3. The figures below present the DC link voltage and current, the low and high voltage sides of the DAB, and variations of batteries parameters. The settling time is 0.0075 s which is shorter compared with PI controller. The variations in the DC link voltage follow those in PI.

Fig. 9. DC link (a) voltage and (b) current under SMC.
Both control methods offer similar performance in the steady state region. However, the SMC can provide better performance with variations rather than the PI. That because of the fast response and the low settling time.

![Fig. 10. Waveforms of primary and secondary voltages using SMC.](image)

![Fig. 11. Voltage, current, and SoC of the batteries of (a) EV1 and (b) EV2.](image)

VI. CONCLUSION

In this paper, a comparison between the PI and sliding mode controllers have been presented for the resonant bidirectional DAB converter. The steps start from designing the resonant tank of DAB converter to deriving the mathematical equations for SMC and then it is implemented using MATLAB/Simulink. The results show that settling time of the SMC is shorter than that of the PI control. Therefore, a better dynamic response can be attained with SMC.

REFERENCES


