Millimetre wave dielectric characterisation of multilayer LTCC substrate


Published in:
Proceedings of the 45th European Microwave Conference

Document Version:
Peer reviewed version

Queen's University Belfast - Research Portal:
Link to publication record in Queen's University Belfast Research Portal

Publisher rights
© 2016 IEEE. Personal use of this material is permitted. Permission from IEEE must be obtained for all other uses, in any current or future media, including reprinting/republishing this material for advertising or promotional purposes, creating new collective works, for resale or redistribution to servers or lists, or reuse of any copyrighted component of this work in other works.

General rights
Copyright for the publications made accessible via the Queen's University Belfast Research Portal is retained by the author(s) and / or other copyright owners and it is a condition of accessing these publications that users recognise and abide by the legal requirements associated with these rights.

Take down policy
The Research Portal is Queen's institutional repository that provides access to Queen's research output. Every effort has been made to ensure that content in the Research Portal does not infringe any person's rights, or applicable UK laws. If you discover content in the Research Portal that you believe breaches copyright or violates any law, please contact openaccess@qub.ac.uk.
Millimetre wave dielectric characterisation of multilayer LTCC substrate

Dmitry Zelenchuk, Vincent Fusco
ECIT, Queen’s University Belfast, Belfast, United Kingdom
d.zelenchuk@qub.ac.uk, v.fusco@qub.ac.uk

James Breslin, Mike Keaveney
Analog Devices Inc., Limerick, Ireland

d.zelenchuk@qub.ac.uk, v.fusco@qub.ac.uk

Abstract—The paper reports on the mm-wave characterization of a low temperature co-fired ceramic (LTCC) substrate. A substrate integrated resonator (SIW) method is presented for robust extraction of both permittivity and loss tangent of the substrate. The data obtained allow full characterization of the substrate in the 71 GHz – 95 GHz frequency range suitable for accurate modelling of E- and W-band printed circuits.

Keywords—dielectric characterisation, LTCC; mm-wave communications

I. INTRODUCTION

Millimeter-wave communications have found many applications in recent years with on-chip V-band systems [1], E-band backhaul systems [2] and W-band automotive sensors [3] being most prominent examples. The onset of 5G communications poses even greater emphasis on development of compact and efficient integrated mm-wave devices [4].

For accurate modelling and design of the devices, it is very important to know the parameters of the materials and have an ability to conduct non-destructive tests on the same substrate.

Recently substrate integrated waveguides have been proposed as means of testing the dielectric properties of PCB substrates [5]–[7]. The method has been shown to be accurate up to mm-wave for low dielectric constant substrate.

In recent years LTCC process has become widely used for mm-wave applications due to stable permittivity, good tolerance of the process and easy integration with active components [8]–[10]. For LTCC substrates with high permittivity direct translation of the previous SIW cavities designs, e.g. from [5], leads to prohibitively small dimensions. In this paper SIW resonator method has been extended for characterization of a multilayer LTCC substrate with high permittivity.

II. DESIGN

A. Theory

A substrate integrated waveguide rectangular cavity is shown in Fig. 1. It has been shown in [11], [12] that both propagation and attenuation constants of a SIW waveguide are equivalent to the quantities of a rectangular waveguide with effective width \( a_{\text{eff}} \),

\[
a_{\text{eff}} = a - 1.08 \frac{d^2}{a} + 0.1 \frac{d^2}{a} \quad (1)
\]

\( a_{\text{eff}} \) is the velocity of light in vacuum and \( \varepsilon_r \) is the relative permittivity of the substrate.

As described in [5], [13] there are three major loss mechanisms associated with SIW: dielectric loss, conductor loss and radiation loss. Therefore, the unloaded quality factor \( Q_u \) of the cavity is:

\[
\frac{1}{Q_u} = \frac{1}{Q_c} + \frac{1}{Q_D} + \frac{1}{Q_{\text{rad}}} \quad (3)
\]

Here \( Q_c \) is the quality factor associated with the conductor loss, whereas \( Q_D \) is the quality factor associated with the dielectric loss, whereas \( Q_{\text{rad}} \) is the quality factor due to radiation loss, which is in case of SIW cavity is manifested by leakage through the via walls. The latter can be designed as a negligible contributor subject to the sufficient via density [5], [13], [14].

LTCC process metallization consists of copper on the outer layers and metal with conductivity twice as low as copper for
inner layers and vias, the electromagnetic properties of the metals are well known.

The quality factor \( Q_c \) for the TE_{110} mode of a rectangular cavity with different wall, top and bottom metallization with surface resistance \( R_{sx}, R_{st}, \) and \( R_{sb} \) respectively reads as [5]:

\[
Q_c = \frac{k_c^2 \eta \pi^{-2}}{2R_{sx}(2n^2b_{eff}^2h + 2ha_{eff}^2)} + \frac{k_c^2 \eta \pi^{-2}}{(R_{st} + R_{sb})(n^2b_{eff}^2 + a_{eff}^2)}
\]

(4)

where

\[ k_c = \sqrt{\frac{(\pi/a_{eff})^2 + (n\pi/b_{eff})^2}{\mu_m \epsilon_r \sqrt{c/\epsilon_r} \Ohm}} \]

\( h \) is the substrate thickness, and \( \eta = 120\pi/\sqrt{\epsilon_r} \Ohm \)

We note that for a rectangular cavity with reduced height, conductor losses in the sidewalls (reciprocal of the first term of (4)) are smaller than those in the top and bottom walls (reciprocal of the second term of (4)).

B. Extraction of Dielectric Properties

Once the resonant frequency and the quality factor of the resonator have been measured, the substrate permittivity \( \epsilon_r \) and the loss tangent \( \tan \delta \) can be obtained

\[
\epsilon_r \text{rect} = \frac{c}{2\pi f_{\text{meas}}} \left( \frac{m\pi}{a_{eff}} \right)^2 \left( \frac{m\pi}{b_{eff}} \right)^2 \frac{1}{Q_u} - \frac{1}{Q_c}
\]

(5)

\[
\tan \delta = \frac{1}{Q_u} - \frac{1}{Q_c}
\]

Importantly, it follows from (5) that the extracted permittivity does not depend on the thickness of the substrate and the metal loss is defined by closed-form accurate expression (4). This makes the presented approach preferable to other planar techniques like microstrip or stripline resonator measurement, where individual designs and subsequent extractions are necessary for each substrate thickness. The radiation loss is discarded in (5) as the proposed method assumes design with negligible leakage.

C. SIW cavity design on LTCC substrate

At mm-wave frequencies there are technological limitations posed on the design of the SIW cavities. These are particularly prominent for higher permittivities of LTCC substrates. Namely the size of a cavity at fundamental mode becomes close to the via period, which can limit applicability of the effective width formula (1) and hence the method itself. It is also important to incorporate a launching mechanism that does not substantially alter the fields distribution in the cavity.

For the particular substrate under test the manufacturer specified nominal values of the permittivity as 7.8 and loss tangent as 0.0014. The via diameter was set to 100 microns with period of 250 microns. As the substrate was subsequently planned for other multilayer applications the two-layer dielectric with thicknesses of 0.22 mm and 0.33 mm was used, see Fig. 2.

First, it is important to choose correct dimensions of the launching structure. Here we use the u-shaped slot, see Fig. 1, as it does allow direct measurement with 50-Ohm GSG probe and eliminates the need for additional calibration of a cable or a transition extensions. In order to accommodate 150 micron GSG probe the dimensions of the u-shaped slot were chosen as \( w_s = w_d = w_u = 50 \mu m \), \( w_e = L_p = 100 \mu m \), \( D_p = 250 \mu m \). It is difficult to make the shape smaller due to dimensions of the probe as well as due to etching tolerances of the process.

The structure is not electrically small in comparison to the wavelength in the dielectric \( \lambda_0 = c/\sqrt{\epsilon_r} \) which is at 90 GHz equals to 1.19 mm. Obviously, at fundamental TE_{110} mode the launcher will introduce significant distortion to the field distribution, that would be difficult to define as a lumped reactance, see [5]. In order to reduce the effect and a higher order resonance should be chosen.

The resonators were designed to operate at TE_{130} mode with estimated value of permittivity of 7.8, see Fig. 3. The position of the launcher was chosen as \( y_l = 7/16 a_{eff} \), which results in the coupling coefficient between 0.1 and 1, recommended in [16] for reduction of measurement errors.

Fig. 2. Layer mapping (“cond” – top metal M1, “cond2”- middle metal “M2”, “resi” – bottom metal M3).

Fig. 3. The field distribution of the TE_{13} resonance mode in an SIW resonator.
III. EXPERIMENTAL RESULTS

The groups of the 8 resonators have been placed in 4 rows: two from each side of the sample. As the board has two dielectric layers with thicknesses of 0.22 mm and 0.33 mm, the arrangement is to test each variation of the material from layer to layer as well as across the layers. The metallization of the outer metal layers is copper $\sigma_{\text{outer}} = 5.8 \times 10^7 \text{S/m}$ and of the inner metal layer (bottom of the resonators) and of the vias is $\sigma_{\text{inner}} = 2.98 \times 10^7 \text{S/m}$.

![Photo of an SIW resonator under test.](image1)

Fig. 4. Photo of an SIW resonator under test.

The measurements were carried out on a Cascade Microtech probe station connected to Agilent PNA E8631C. Each resonator was measured with Cascade Infinity GSG 150 micron pitch probe. An SIW resonator under test is shown in Fig. 4. The measured $S_{11}$ for a resonator with length of 1.91mm is demonstrated in Fig. 5. One can observe no significant measurement uncertainties as all the measured points lie on the fitted circle [16].

The extraction of the quality factor is done following procedures given in [5], [17]. As shown in in Fig. 6, the unloaded quality factor $Q_0$ extracted from the measurements varies across the samples and close to 200, whereas calculated quality factor due to conductor losses $Q_c$ steadily increase with frequency from 500 to 600.

![Smith chart of measured resonator with length of 1.91mm.](image2)

Fig. 5. Smith chart of measured resonator with length of 1.91mm.

![Quality factor of the SIW resonators placed on 0.22 mm thick substrate.](image3)

Fig. 6. Quality factor of the SIW resonators placed on 0.22 mm thick substrate.

The extracted permittivity and loss tangent are plotted in Fig. 7 and Fig. 8. The four batches of the resonators can be separated into two groups as 'siw' and 'asiw' batches are on the bottom 0.22 mm thick layer and the batches 'bsiw' and 'cswi' are on the top 0.33 mm thick layer. The batches on the same side of the board are shifted 5 cm from each other in order to test consistency of the parameters across the board.

![Extracted permittivity.](image4)

Fig. 7. Extracted permittivity.

![Extracted loss tangent.](image5)

Fig. 8. Extracted loss tangent.

It follows from the measured permittivity that resonators that placed on the 0.33 mm thick layer have average value of 8.03 whereas at 0.22mm thick layer the value is 7.95. It has to be noted that the permittivity is stable with frequency across the whole 71-95 GHz frequency range.
The loss tangent varies between 0.003 and 0.004, which is significantly higher than nominal value of 0.0014. The discrepancy requires further investigation as neither radiation nor metal losses were found to accommodate the difference.

Comparison of the results from the batches from the same side of the board one may also conclude that there is very little variation of the permittivity and loss tangent across the board. The latter finding supports the choice of the material as suitable candidate for mm-wave applications.

IV. CONCLUSION

A novel SIW resonator method has been proposed for characterization of multilayer LTCC substrate at mm-waves. There were four batches of the resonators designed and measured. The extracted permittivity and loss tangent are consistent across the batches in the frequency range from 71 GHz to 95 GHz. The permittivity is found to be stable across the frequency range with average value of 7.95 at 0.22 mm thick substrate and 8.03 at 0.33 mm thick substrate.

ACKNOWLEDGMENT

The authors are grateful to Analog Devices Inc for facilitating the manufacturing process and TDK for manufacturing of the samples and providing the data on the manufacturing process.

REFERENCES


